

P-Ch 20V Fast Switching MOSFETs

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary

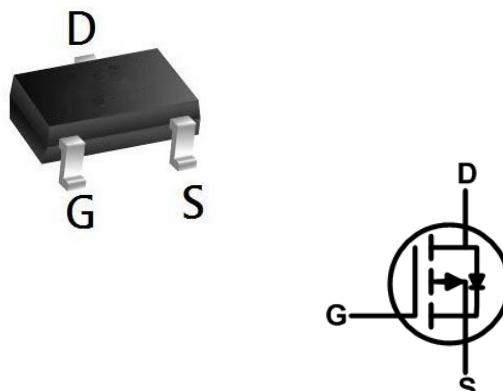


BVDSS	RDS(on)	ID
-20V	65 mΩ	-3.3A

Description

The XR3401C is the high cell density trenched P-ch MOSFETs, which provides excellent RDS(on) and efficiency for most of the small power switching and load switch applications. The XR3401C meet the RoHS and Green product requirement with full function reliability approved.

SOT23 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-20	V
V _{GS}	Gate-Source Voltage	±12	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -4.5V ¹	-3.3	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -4.5V ¹	-2.5	A
I _{DM}	Pulsed Drain Current ²	-12	A
P _D @T _A =25°C	Total Power Dissipation ³	1	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	125	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	80	°C/W

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Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=-250\mu\text{A}$	-20	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$\text{V}_{\text{GS}}=-4.5\text{V}$, $\text{I}_D=-3\text{A}$	---	65	75	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-2.5\text{V}$, $\text{I}_D=-2\text{A}$	---	75	85	
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_D=-250\mu\text{A}$	-0.3	-0.5	-1.0	V
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=-16\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=25^\circ\text{C}$	---	---	-1	uA
		$\text{V}_{\text{DS}}=-16\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=55^\circ\text{C}$	---	---	-5	
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 12\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=-5\text{V}$, $\text{I}_D=-3\text{A}$	---	12.2	---	S
Q_{g}	Total Gate Charge (-4.5V)	$\text{V}_{\text{DS}}=-15\text{V}$, $\text{V}_{\text{GS}}=-4.5\text{V}$, $\text{I}_D=-3\text{A}$	---	10.1	---	nC
Q_{gs}	Gate-Source Charge		---	1.21	---	
Q_{gd}	Gate-Drain Charge		---	2.46	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=-10\text{V}$, $\text{V}_{\text{GS}}=-4.5\text{V}$, $\text{R}_G=3.3\Omega$	---	5.6	---	ns
T_r	Rise Time		---	32.2	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	45.6	---	
T_f	Fall Time		---	29.2	---	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=-15\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	677	---	pF
C_{oss}	Output Capacitance		---	82	---	
C_{rss}	Reverse Transfer Capacitance		---	73	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,4}	$\text{V}_G=\text{V}_D=0\text{V}$, Force Current	---	---	-3.3	A
V_{SD}	Diode Forward Voltage ²	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_s=-1\text{A}$, $\text{T}_J=25^\circ\text{C}$	---	---	-1	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

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Typical Characteristics

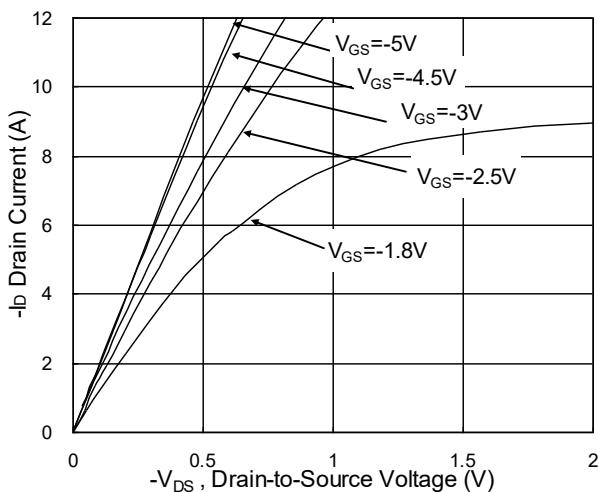


Fig.1 Typical Output Characteristics

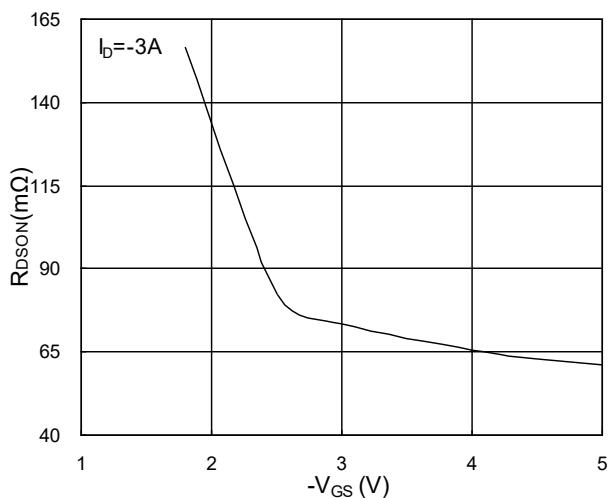


Fig.2 On-Resistance vs. Gate-Source Voltage

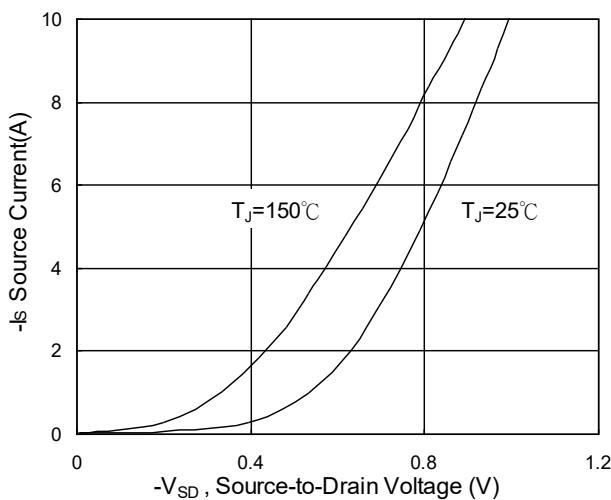


Fig.3 Forward Characteristics of Reverse

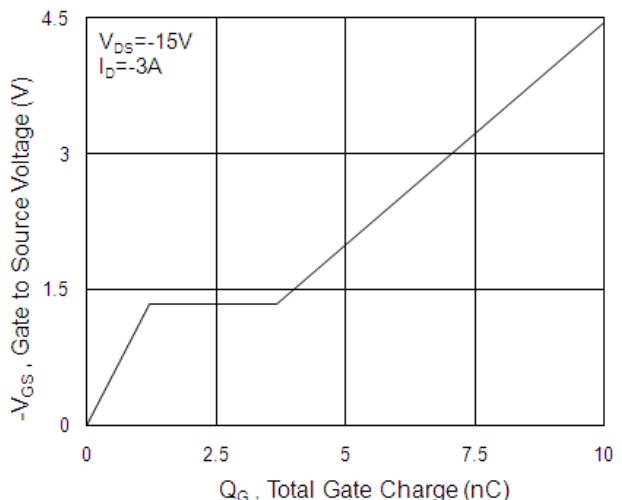
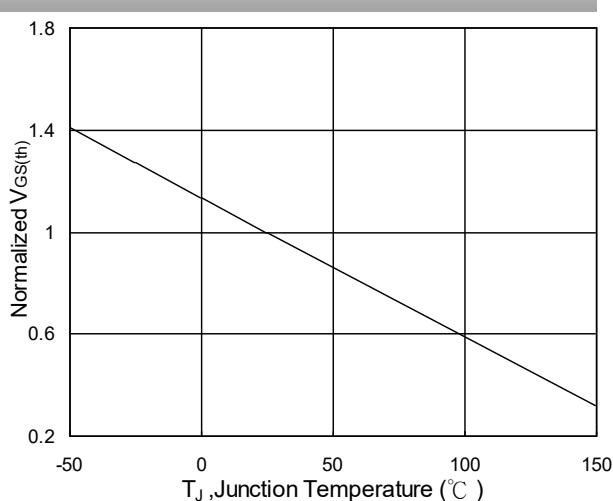
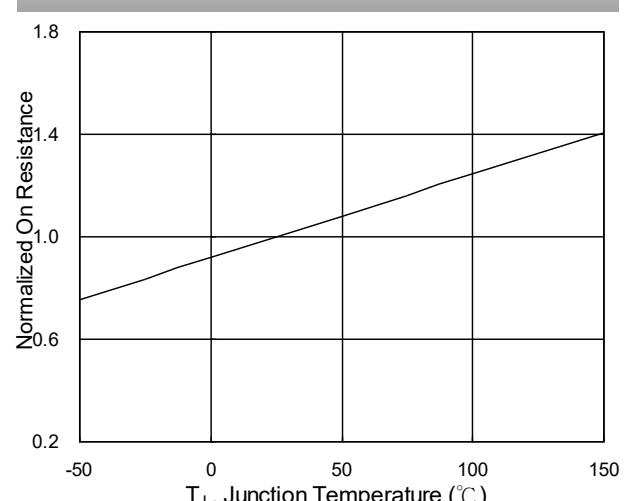


Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ vs. T_J Fig.6 Normalized R_{DSON} vs. T_J

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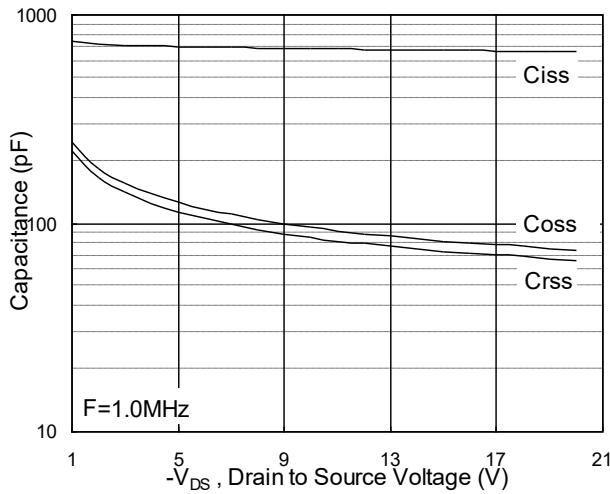


Fig.7 Capacitance

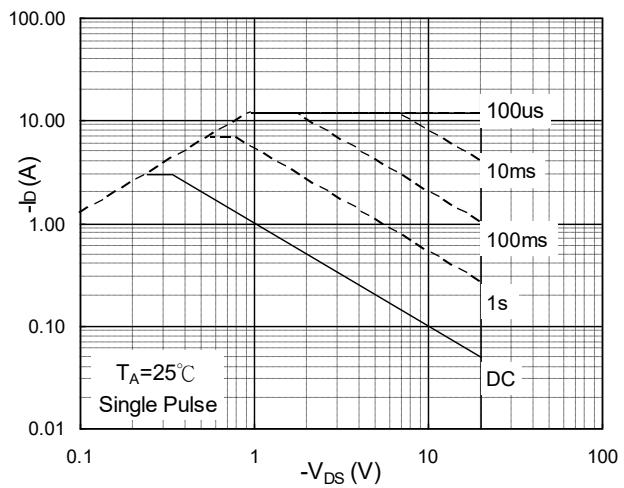


Fig.8 Safe Operating Area

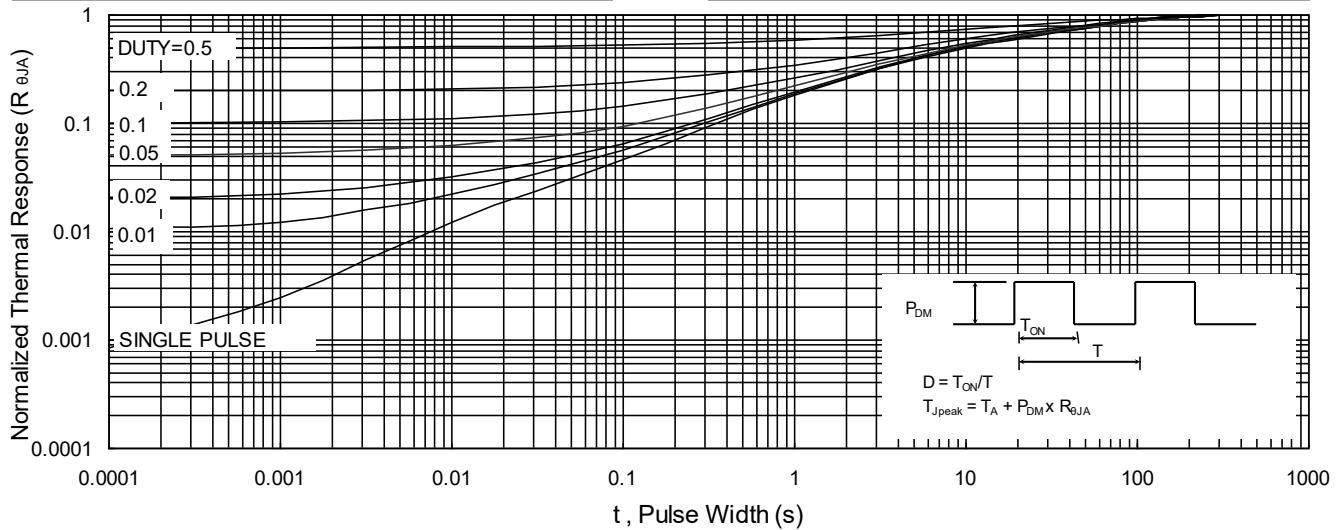


Fig.9 Normalized Maximum Transient Thermal Impedance

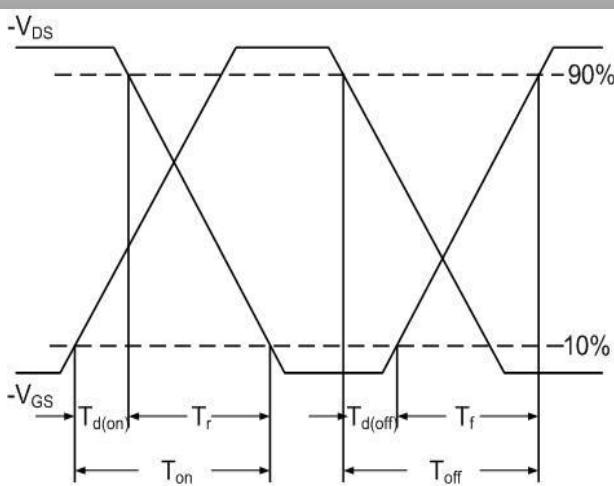


Fig.10 Switching Time Waveform

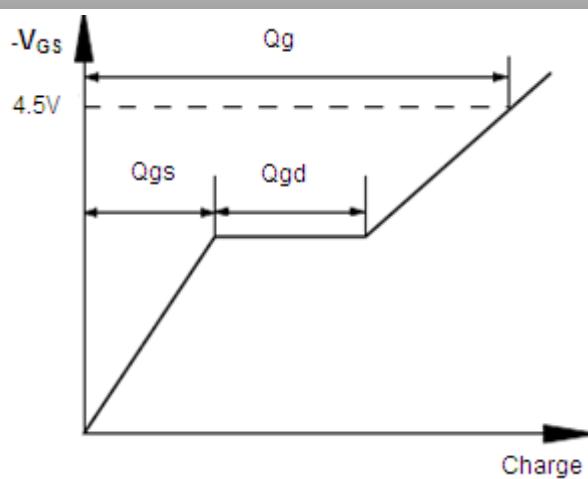
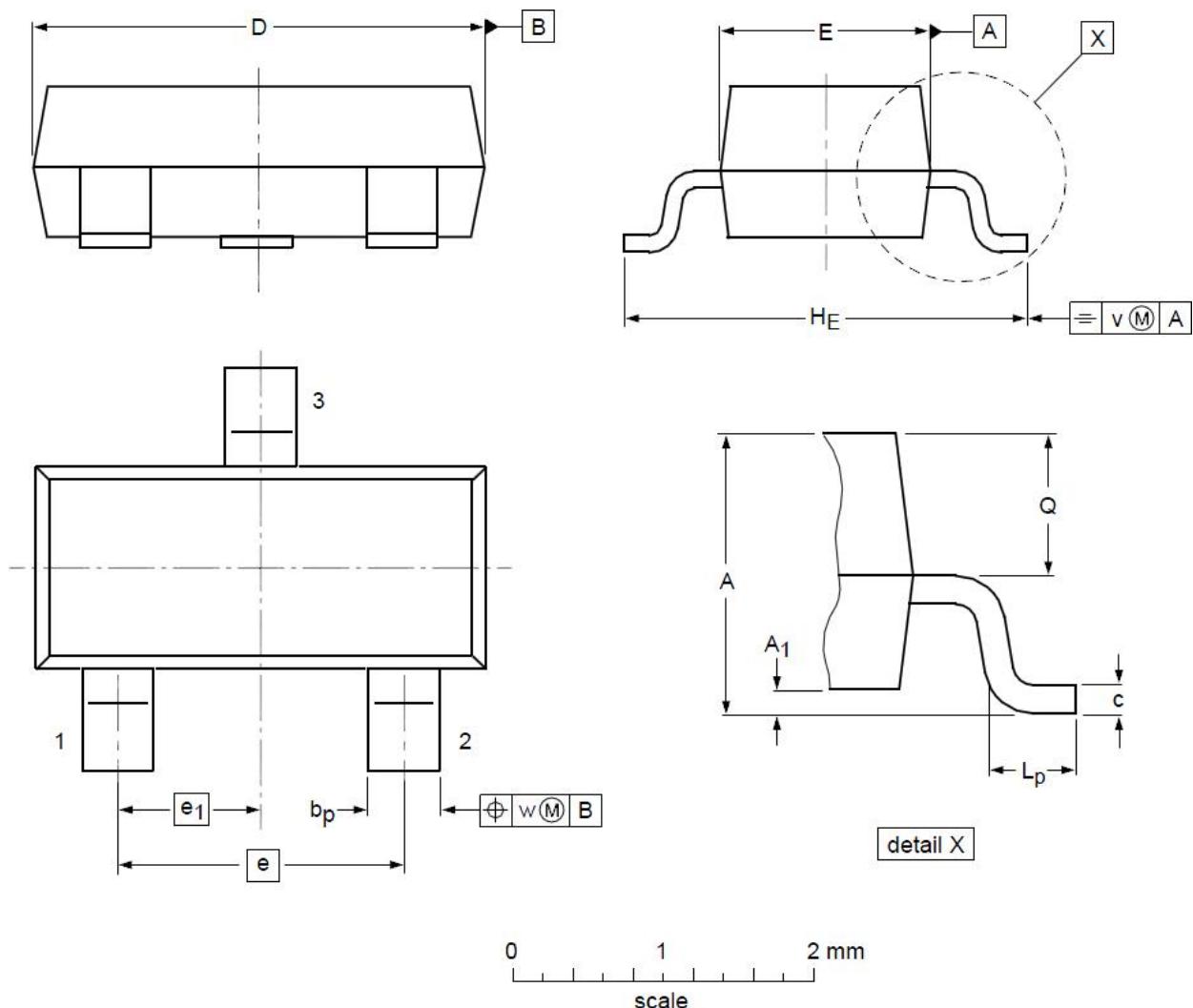


Fig.11 Gate Charge Waveform

Package Mechanical Data-SOT-23



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	A₁	0.01	0.05	0.10
b_p	0.30	0.42	0.50	c	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
e	--	1.90	--	e₁	--	0.95	--
H_E	2.25	2.40	2.55	L_p	0.30	0.42	0.50
Q	0.45	0.49	0.55	v	--	0.20	--
w	--	0.10	--				