

## N-Ch 100V Fast Switching MOSFETs

- ★ Super Low Gate Charge
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

## Product Summary



BVDSS	RDS(on)	ID
100V	88 mΩ	10A

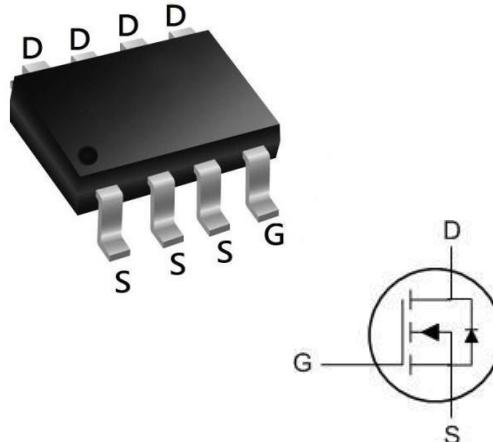
## Description

The XR10N10S is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDS(on) and gate charge for most of the synchronous buck converter applications.

The XR10N10S meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

## Absolute Maximum Ratings

## SOP8 Pin Configuration



Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	100	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	10	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	5	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	10	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	6.1	mJ
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>3</sup>	5	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>	---	125	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	3.6	°C/W

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Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise specified)

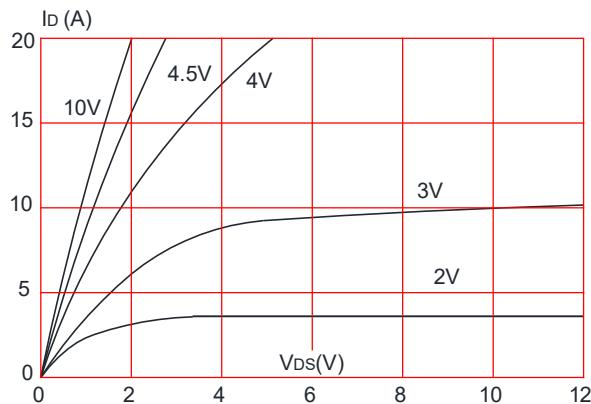
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristics</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	100	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=100\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	1.0	1.5	2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$ , $I_D=3\text{A}$	-	88	115	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=2\text{A}$	-	100	140	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	610	-	pF
$C_{oss}$	Output Capacitance		-	40	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	25	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=50\text{V}$ , $I_D=2\text{A}$ , $V_{GS}=10\text{V}$	-	12	-	nC
$Q_{gs}$	Gate-Source Charge		-	2.2	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	2.5	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=50\text{V}$ , $I_D=3\text{A}$ , $R_G=1.8\Omega$ , $V_{GS}=10\text{V}$	-	7	-	ns
$t_r$	Turn-on Rise Time		-	5	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	16	-	ns
$t_f$	Turn-off Fall Time		-	6	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current		-	-	3	A
$I_{sM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	12	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=3\text{A}$	-	-	1.2	V
$trr$	Body Diode Reverse Recovery Time	$I_F=3\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	-	21	-	ns
$Qrr$	Body Diode Reverse Recovery Charge		-	21	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

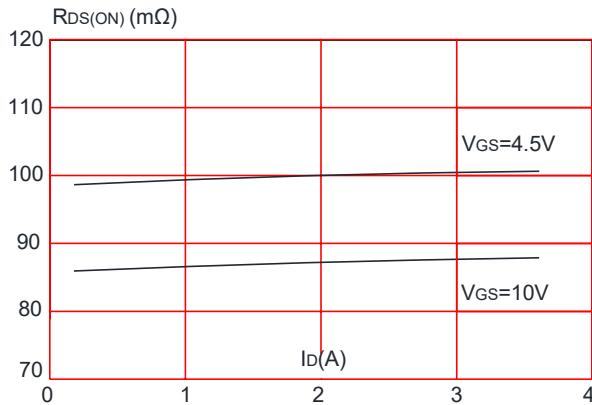
2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{DD}=50\text{V}$ ,  $V_G=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_g=25\Omega$ ,  $I_{AS}=4\text{A}$ 3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$

## Typical Performance Characteristics

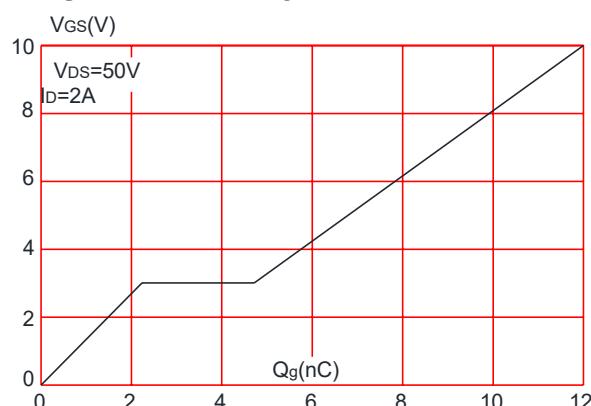
**Figure 1:** Output Characteristics



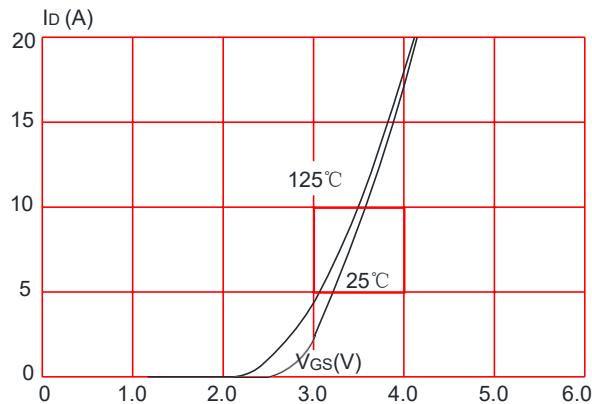
**Figure 3:** On-resistance vs. Drain Current



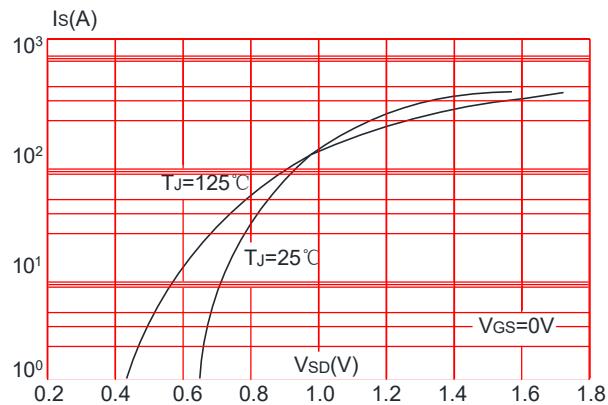
**Figure 5:** Gate Charge Characteristics



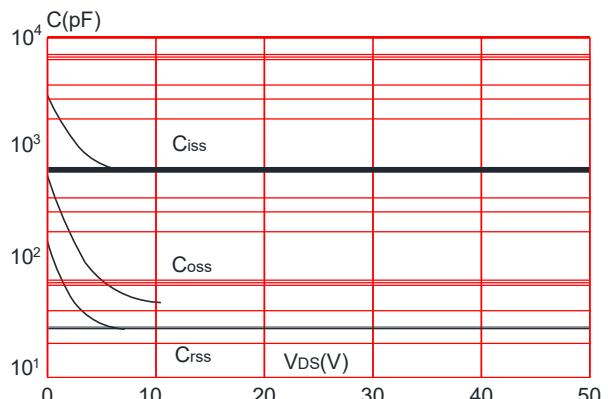
**Figure 2:** Typical Transfer Characteristics



**Figure 4:** Body Diode Characteristics

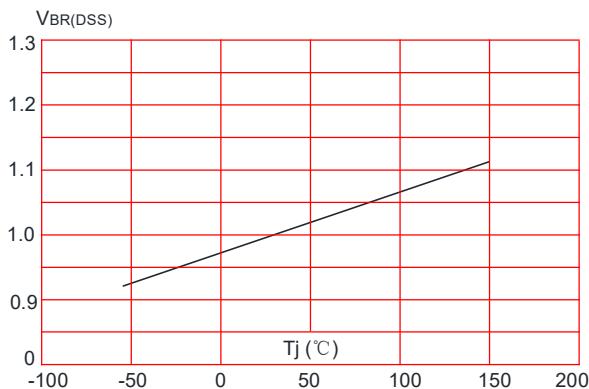


**Figure 6:** Capacitance Characteristics

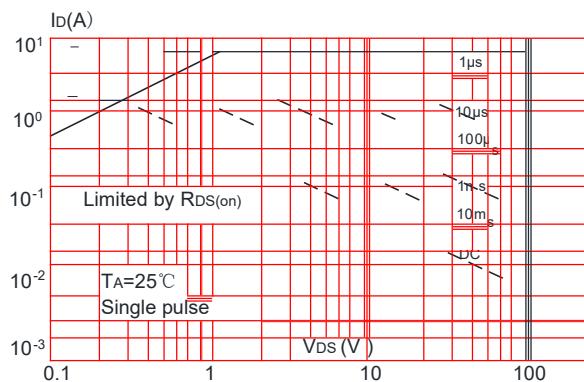


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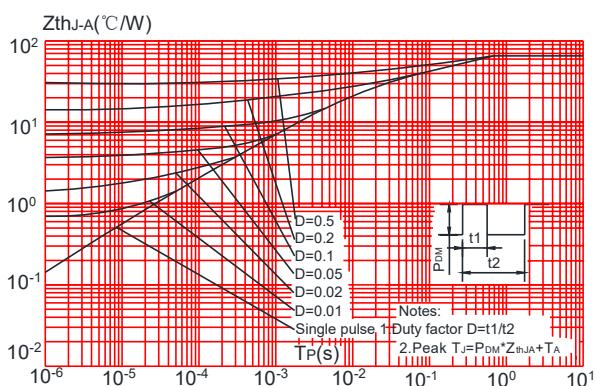
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



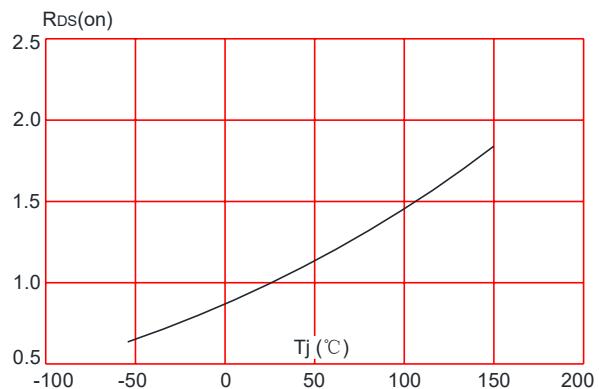
**Figure 9:** Maximum Safe Operating Area



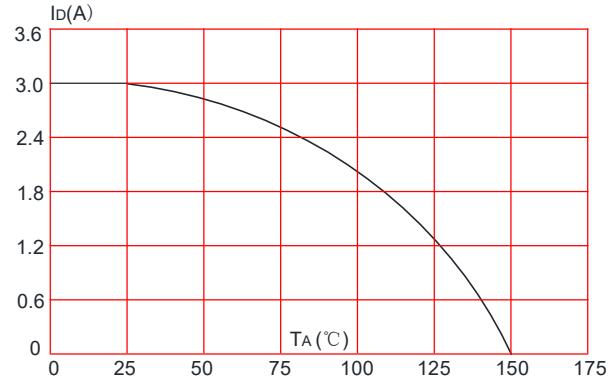
**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



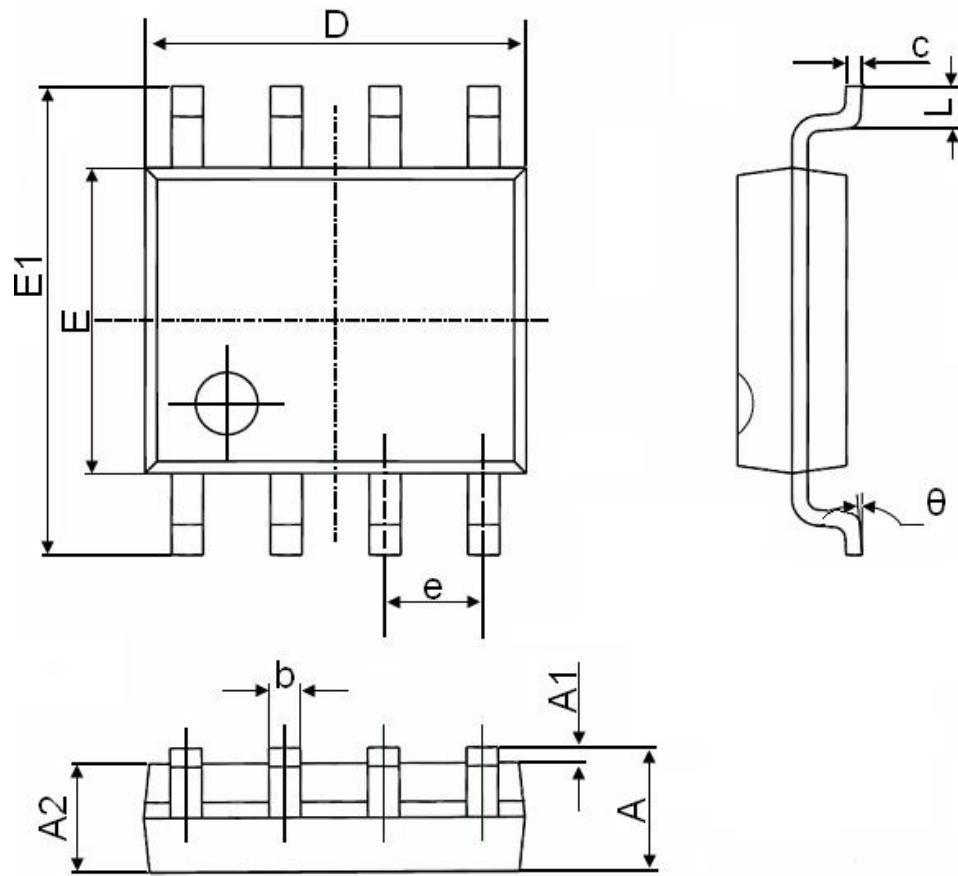
**Figure 8:** Normalized on Resistance vs. Junction Temperature



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



## SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°