

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

General Description

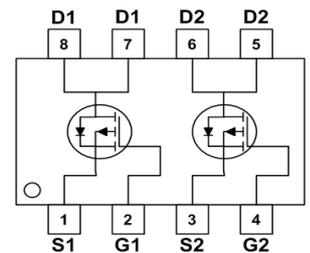
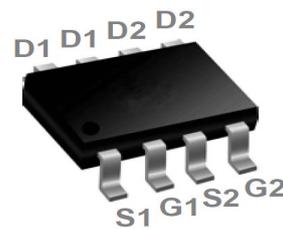
The XR4953C is the highest performance trench P-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The XR4953C meet the RoHS and Green Product requirement with full function reliability approved.

Product Summary

BVDSS	RDSON	ID
-20V	80mΩ	-3A

SOP-8 Pin Configuration



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Symbol	Parameter	Max.	Units
V _{DSS}	Drain-Source Voltage	-20	V
V _{GSS}	Gate-Source Voltage	±12	V
I _D	Continuous Drain Current	T _A = 25°C	-3.0
		T _A = 100°C	-2.0
I _{DM}	Pulsed Drain Current ^{note1}	-10	A
P _D	Power Dissipation	1	W
R _{θJA}	Thermal Resistance, Junction to Ambient	125	°C/W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C

Electrical Characteristics (T_C=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = -250μA	-20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -20V, V _{GS} = 0V,	-	-	-1	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±12V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-0.4	-0.7	-1.0	V
R _{DS(on)}	Static Drain-Source on-Resistance <small>note2</small>	V _{GS} =-4.5V, I _D =-2.5A	-	80	104	mΩ
		V _{GS} =-2.5V, I _D =-1.5A	-	110	154	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz	-	248	-	pF
C _{oss}	Output Capacitance		-	42	-	pF
C _{rss}	Reverse Transfer Capacitance		-	31	-	pF
Q _g	Total Gate Charge	V _{DS} = -10V, I _D = -2.5A, V _{GS} = -4.5V	-	2.9	-	nC
Q _{gs}	Gate-Source Charge		-	0.45	-	nC
Q _{gd}	Gate-Drain("Miller") Charge		-	0.75	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} = -10V, R _L =5Ω, R _{GEN} =3Ω, V _{GS} =-4.5V,	-	9.8	-	ns
t _r	Turn-on Rise Time		-	4.9	-	ns
t _{d(off)}	Turn-off Delay Time		-	20.5	-	ns
t _f	Turn-off Fall Time		-	7	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-3.0	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-10	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = -2.5A	-	-	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

Figure 1: Output Characteristics

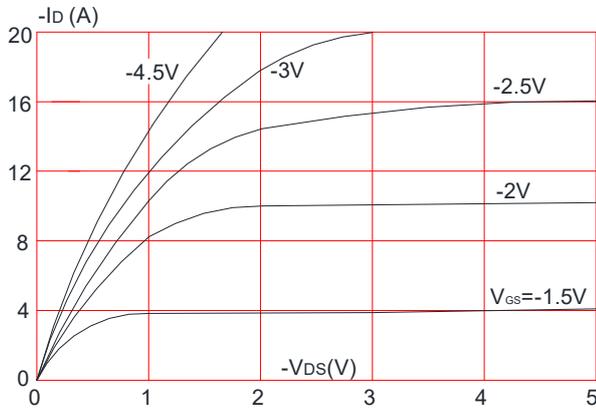


Figure 2: Typical Transfer Characteristics

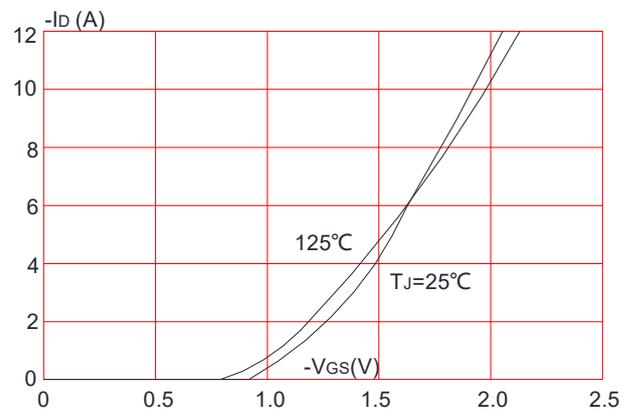


Figure 3: On-resistance vs. Drain Current

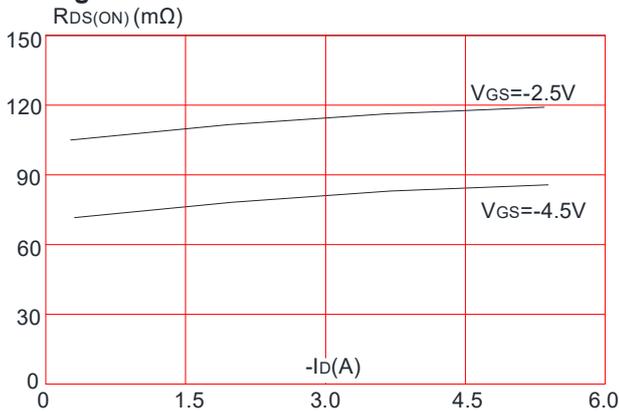


Figure 4: Body Diode Characteristics

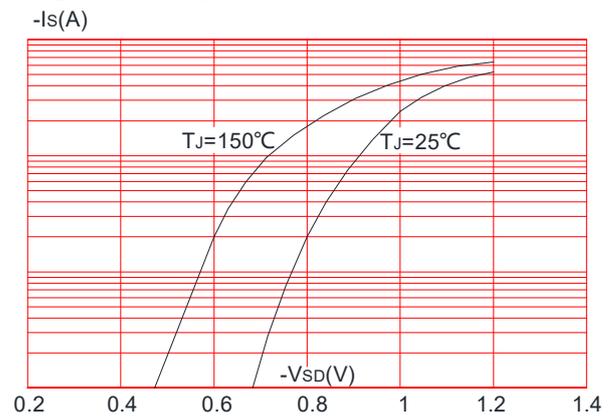


Figure 5: Gate Charge Characteristics

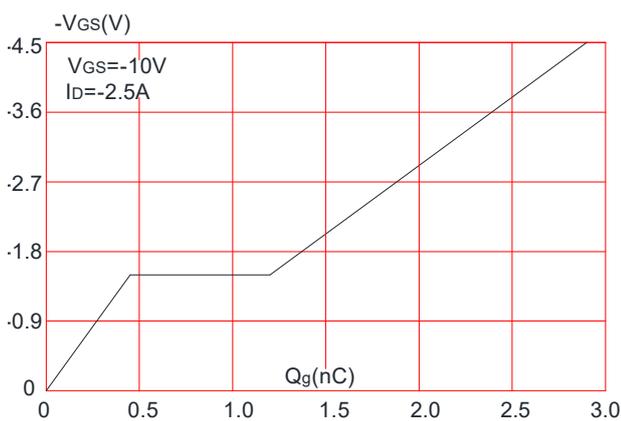


Figure 6: Capacitance Characteristics

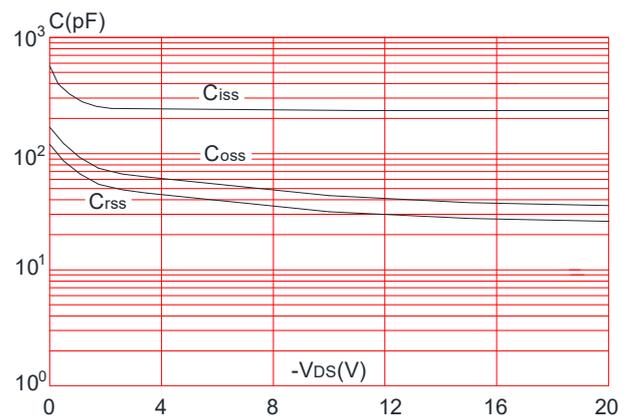


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

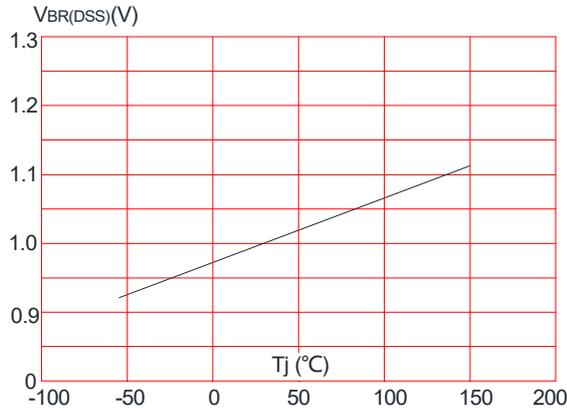


Figure 8: Normalized on Resistance vs. Junction Temperature

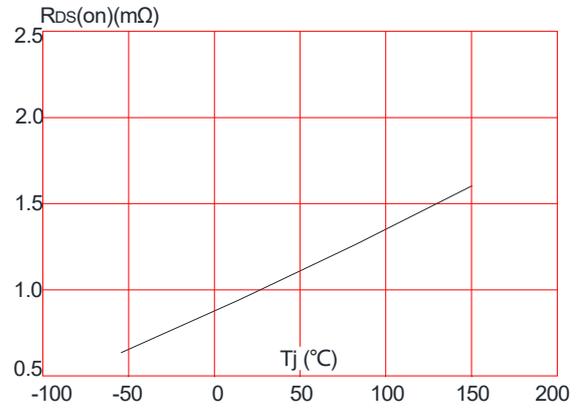


Figure 9: Maximum Safe Operating Area

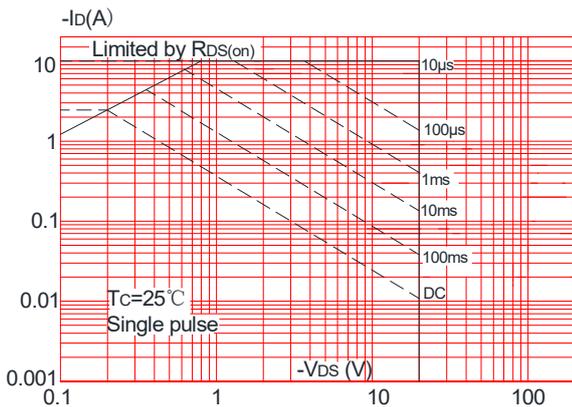


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

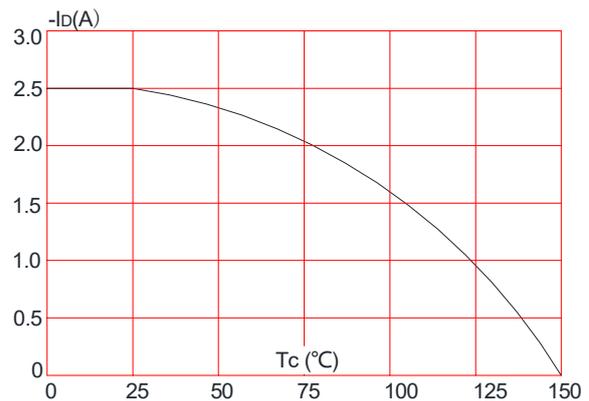
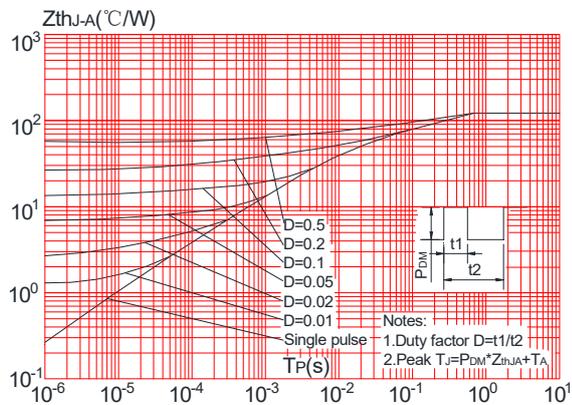
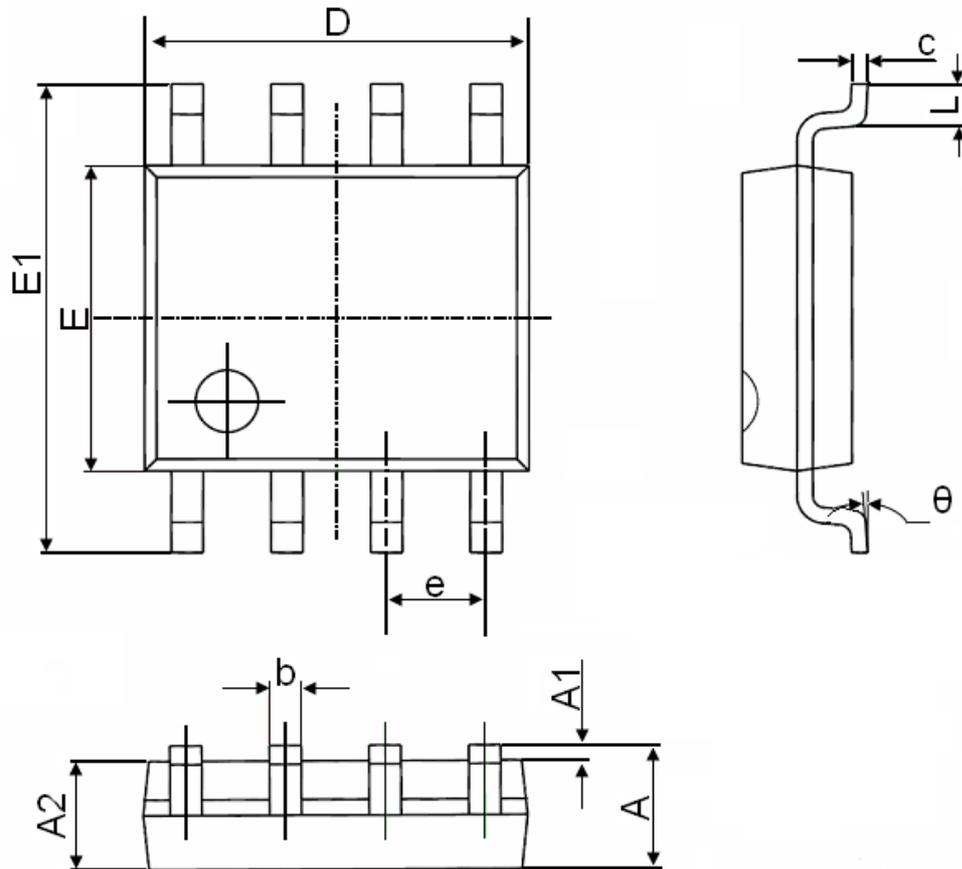


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°