

**Features**

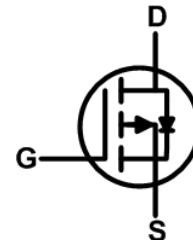
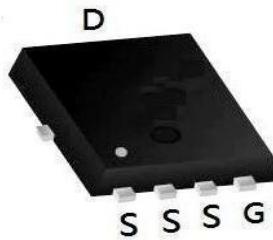
- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- Green Device Available

Product Summary

BVDSS	RDS(ON)	ID
-150V	650mΩ	-2.2A

Applications

- Load Switch.
- Power Management.
- LED Backlighting.
- Networking Application.

PDFN3333-8 Pin Configuration**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c=25^\circ C$	Continuous Drain Current, $-V_{GS} @ -10V^1$	-2.2	A
$I_D @ T_c=100^\circ C$	Continuous Drain Current, $-V_{GS} @ -10V^1$	-1.5	A
I_{DM}	Pulsed Drain Current ²	-8	A
EAS	Single Pulse Avalanche Energy ³	12.5	mJ
I_{AS}	Avalanche Current	5	A
$P_D @ T_c=25^\circ C$	Total Power Dissipation ⁴	7.8	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	16	°C/W

P-Ch 150V Fast Switching MOSFETs

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

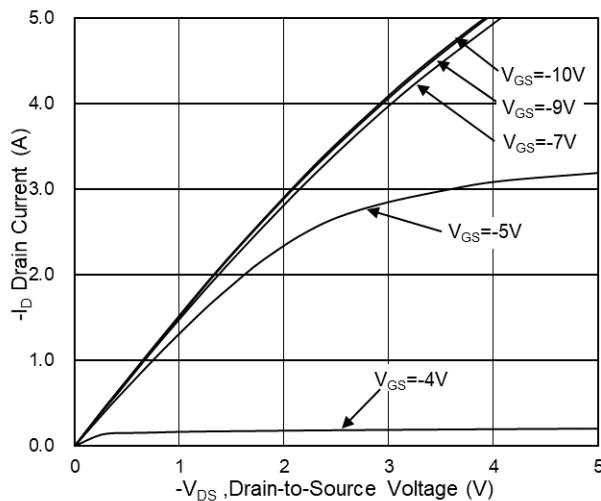
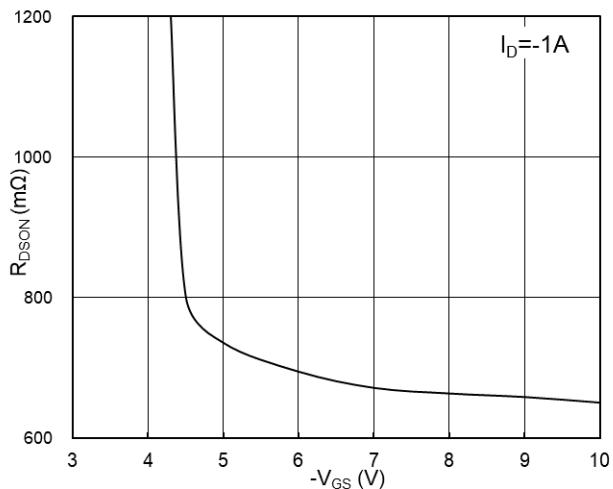
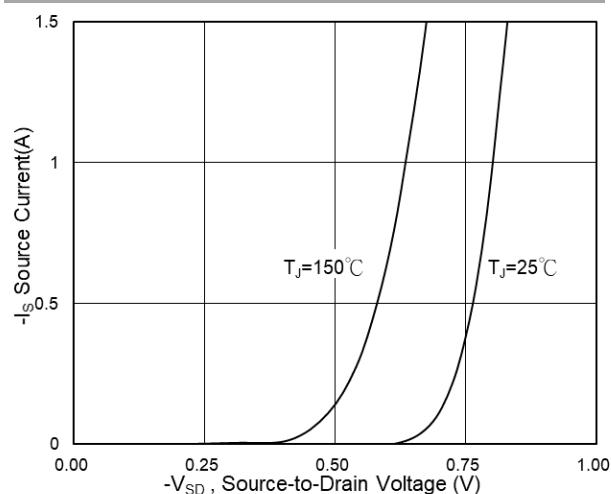
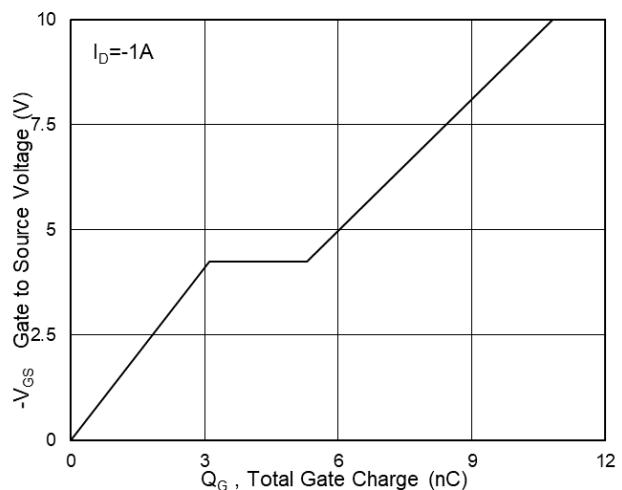
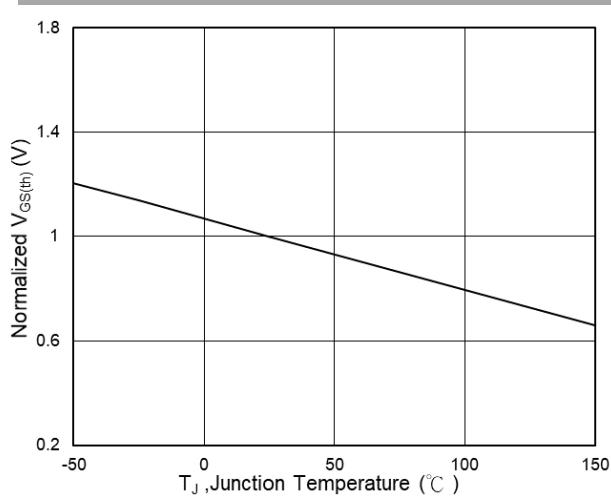
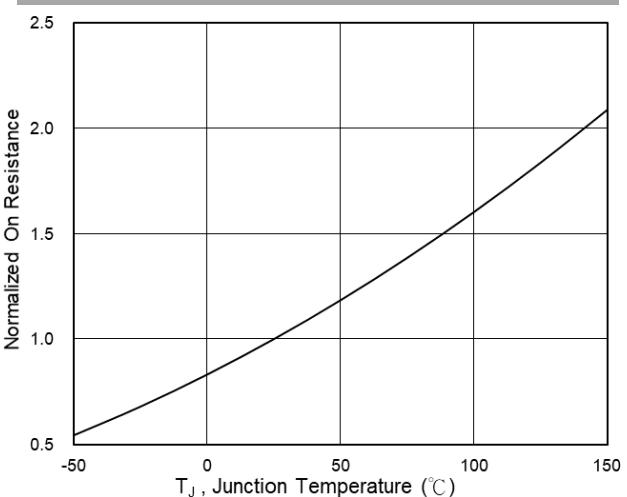
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-150	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=-10\text{V}$, $I_D=-1\text{A}$	---	650	780	$\text{m}\Omega$
		$V_{\text{GS}}=-6\text{V}$, $I_D=-0.5\text{A}$	---	700	980	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	-2.0	-3.0	-4.0	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-120\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=-120\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=85^\circ\text{C}$	---	---	30	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	5	12	20	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=-75\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_D=-1\text{A}$	6.5	10.8	15.2	nC
Q_{gs}	Gate-Source Charge		1.6	3.1	4.7	
Q_{gd}	Gate-Drain Charge		1.1	2.2	3.3	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_G=6\Omega$, $I_D=-1\text{A}$	---	21	32	ns
T_r	Rise Time		---	16	24	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	40	60	
T_f	Fall Time		---	18	27	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-75\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	424	706	988	pF
C_{oss}	Output Capacitance		12	23	35	
C_{rss}	Reverse Transfer Capacitance		7	13	20	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5}	$V_G=V_D=0\text{V}$, Force Current	---	---	-2.2	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=-50\text{V}$, $V_{\text{GS}}=-10\text{V}$, $L=1\text{mH}$, $I_{\text{AS}}=-5\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics**Fig.1 Typical Output Characteristics****Fig.2 On-Resistance vs G-S Voltage****Fig.3 Source Drain Forward Characteristics****Fig.4 Gate-Charge Characteristics****Fig.5 Normalized $V_{GS(th)}$ vs T_J** **Fig.6 Normalized $R_{DS(on)}$ vs T_J**

P-Ch 150V Fast Switching MOSFETs

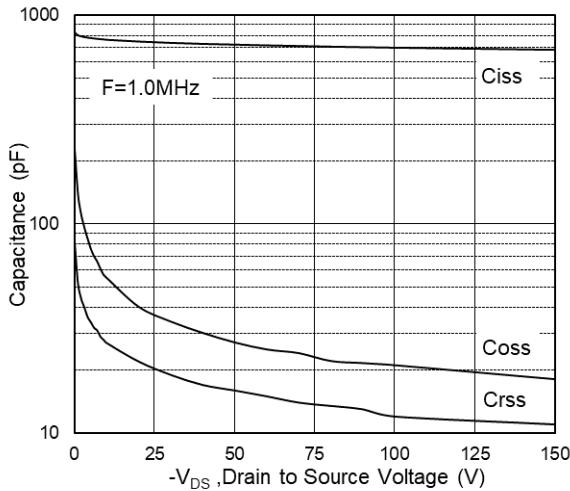


Fig.7 Capacitance

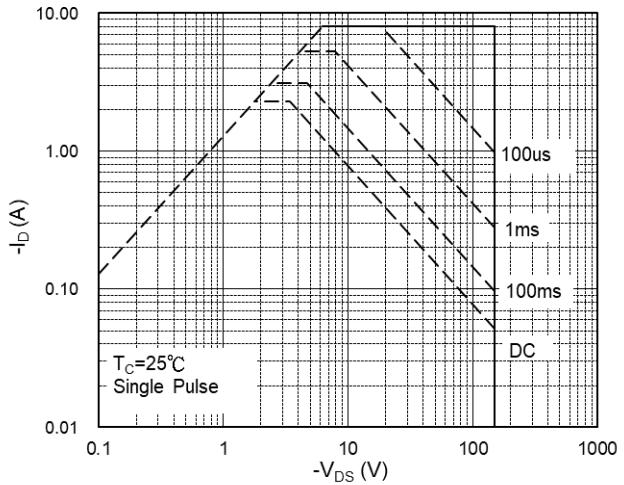


Fig.8 Safe Operating Area

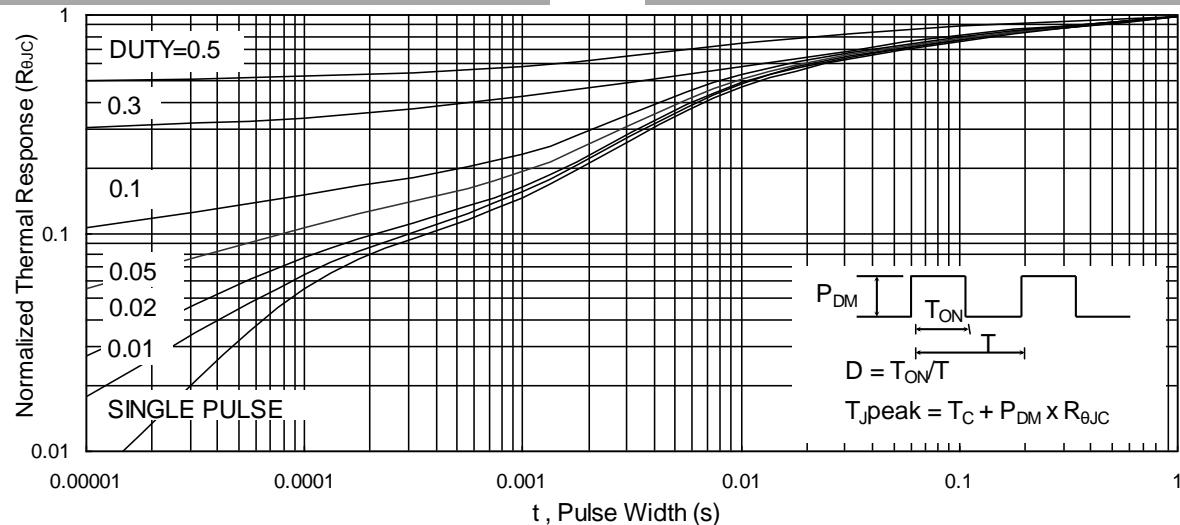


Fig.9 Normalized Maximum Transient Thermal Impedance

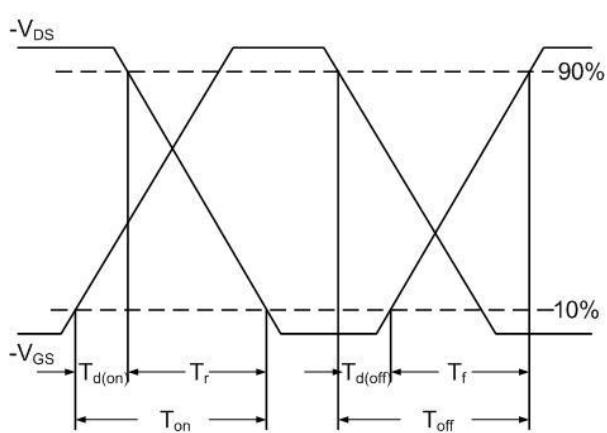


Fig.10 Switching Time Waveform

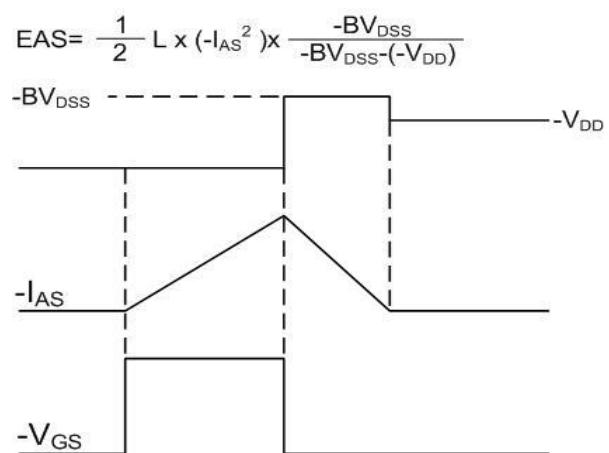
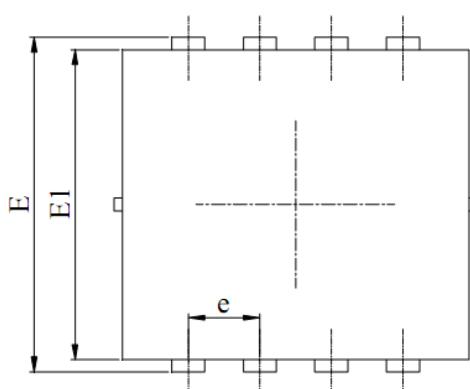
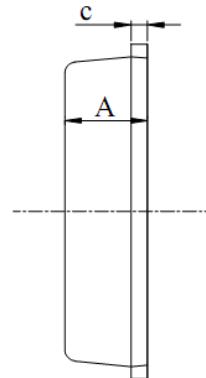


Fig.11 Unclamped Inductive Waveform

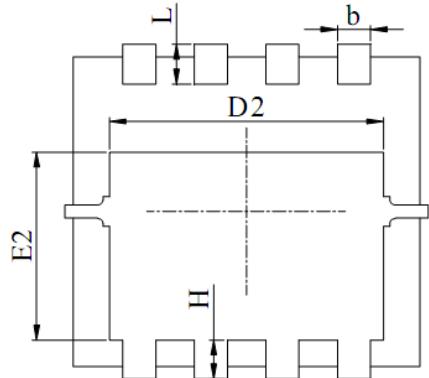
Package Mechanical Data-PDFN3333-8L-Single



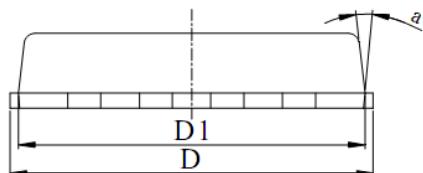
Top View



Side View



Bottom View

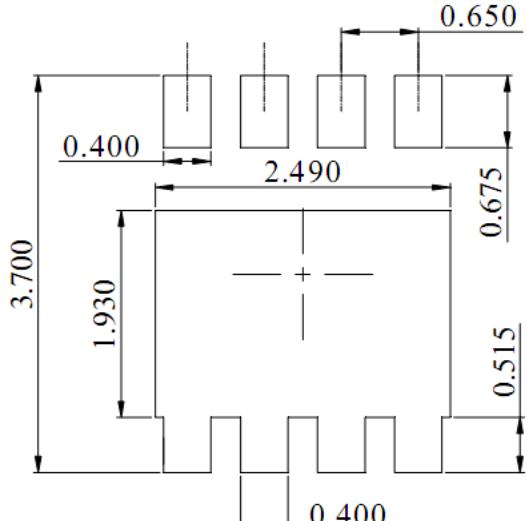


Front View

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
2. ALL DIMNESIONS IN MILLIMETER (ANGLE IN DEGREE).
3. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS.

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.20	0.25
D	3.00	3.15	3.25
D1	2.95	3.05	3.15
D2	2.39	2.49	2.59
E	3.20	3.30	3.40
E1	2.95	3.05	3.15
E2	1.70	1.80	1.90
e	0.65 BSC		
H	0.30	0.40	0.50
L	0.25	0.40	0.50
a	---	---	15°



DIMENSIONS:MILLIMETERS