

**Features**

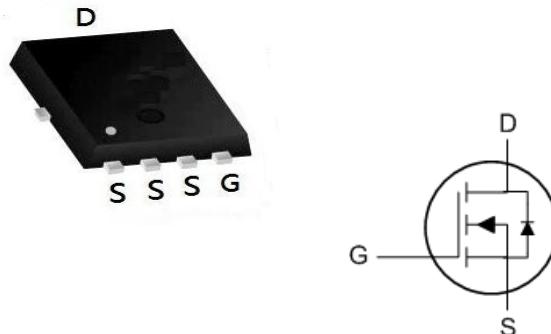
- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low  $R_{DS(ON)}$

**Product Summary**

BVDSS	RDS(on)	ID
40V	5.8mΩ	65A

**Applications**

- DC-DC Converters
- Power management functions
- Synchronous-rectification applications

**PDFN3333-8L Pin Configuration****Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current <sup>1</sup>	65	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current <sup>1</sup>	35	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	130	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	48	mJ
$I_{AS}$	Avalanche Current	35	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation <sup>4</sup>	39	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	---	60	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	3.2	°C/W

Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	40	---	---	V
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10\text{V}$ , $I_D=12\text{A}$	---	5.8	7.8	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=10\text{A}$	---	8.2	13.3	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	1.35	---	3	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=32\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\text{uA}$
		$V_{DS}=32\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=55\text{ }^{\circ}\text{C}$	---	---	5	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA
$R_g$	Gate Resistance	$V_{DS}=0\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	---	1.7	---	$\Omega$
$Q_g$	Total Gate Charge (4.5V)	$V_{DS}=20\text{V}$ , $V_{GS}=4.5\text{V}$ , $I_D=12\text{A}$	---	28	---	$\text{nC}$
$Q_{gs}$	Gate-Source Charge		---	4	---	
$Q_{gd}$	Gate-Drain Charge		---	6	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=15\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=3.3\Omega$ $I_D=1\text{A}$	---	14.3	---	$\text{ns}$
$T_r$	Rise Time		---	5.6	---	
$T_{d(off)}$	Turn-Off Delay Time		---	20	---	
$T_f$	Fall Time		---	11	---	
$C_{iss}$	Input Capacitance	$V_{DS}=15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	---	1048	---	$\text{pF}$
$C_{oss}$	Output Capacitance		---	187	---	
$C_{rss}$	Reverse Transfer Capacitance		---	178	---	

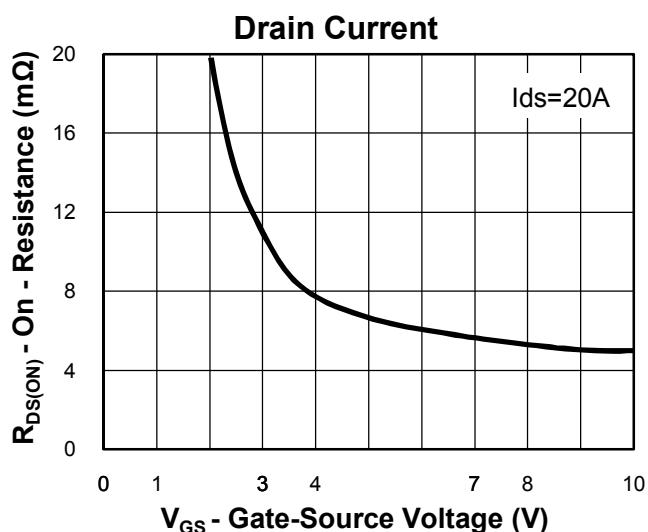
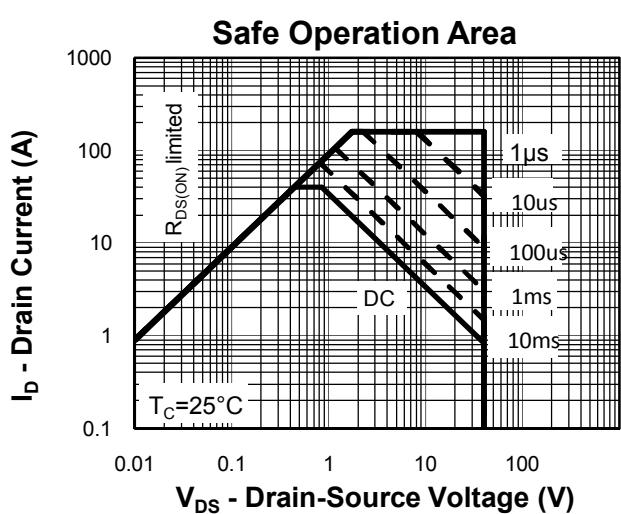
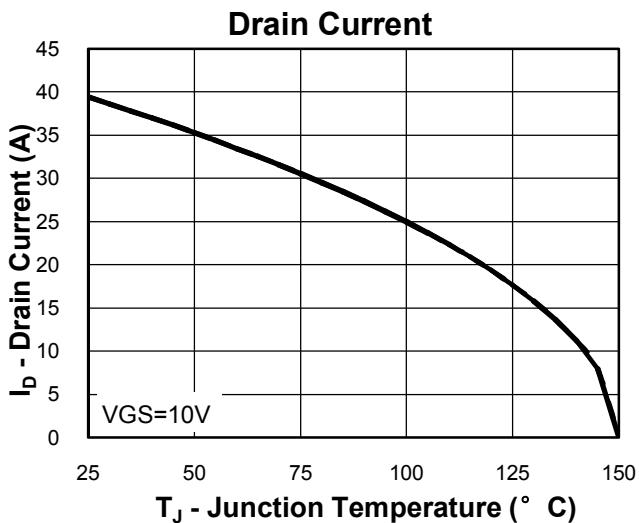
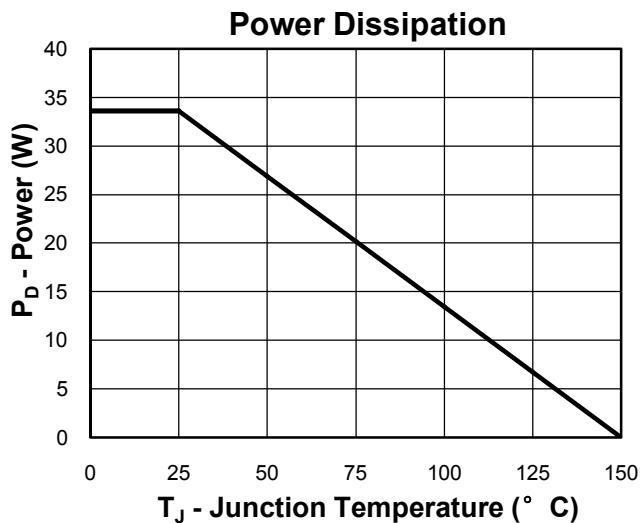
## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	65	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

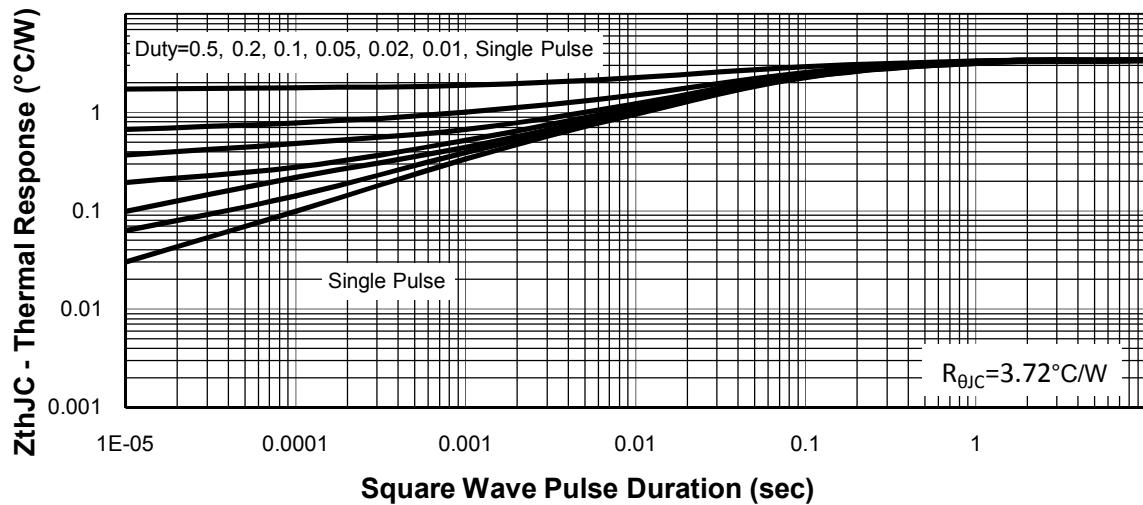
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}=25\text{V}$ , $V_{GS}=10\text{V}$ , $L=0.1\text{mH}$ , $I_{AS}=31\text{A}$
- 4.The power dissipation is limited by  $150\text{ }^{\circ}\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

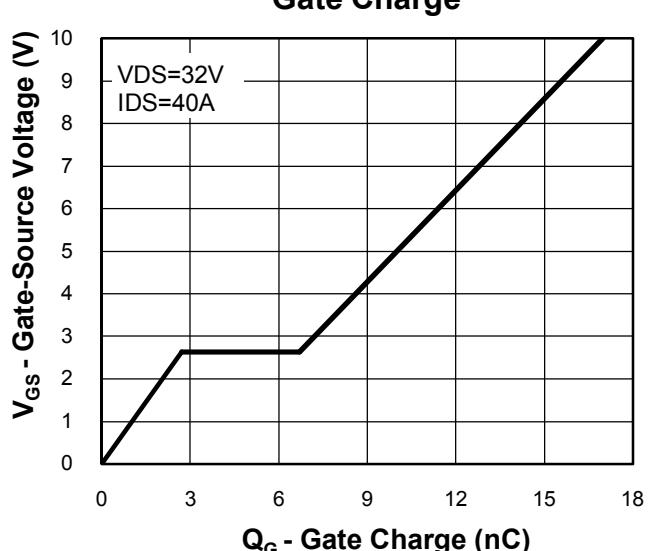
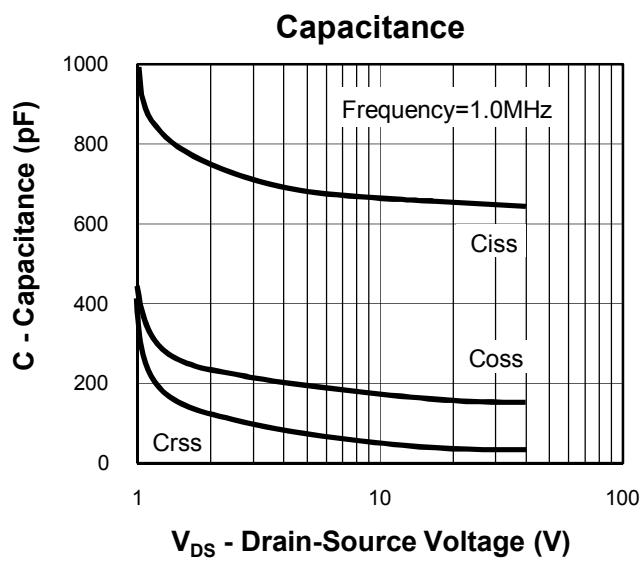
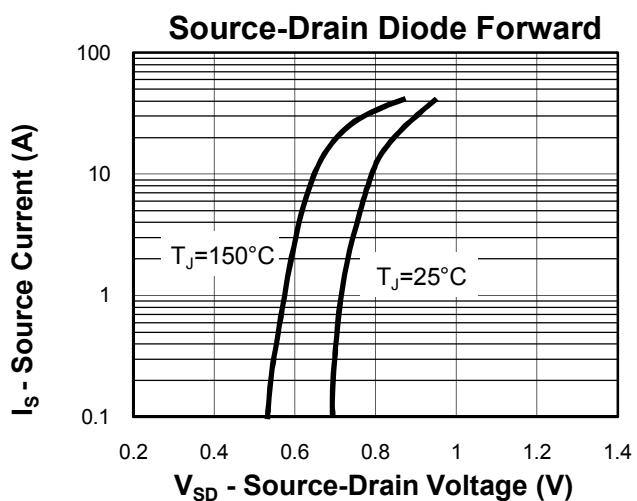
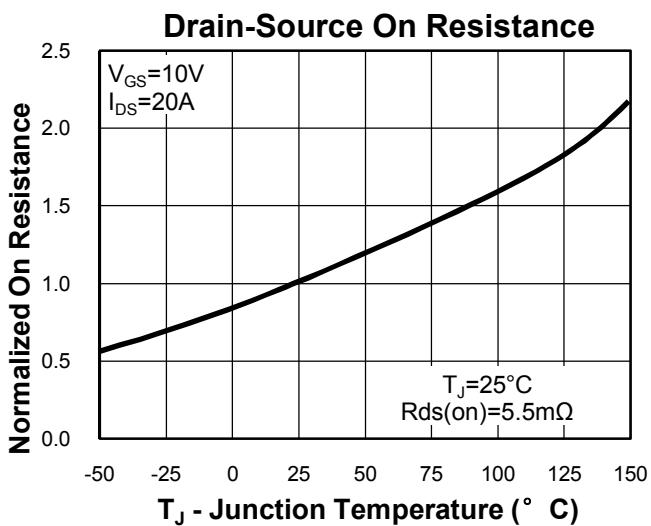
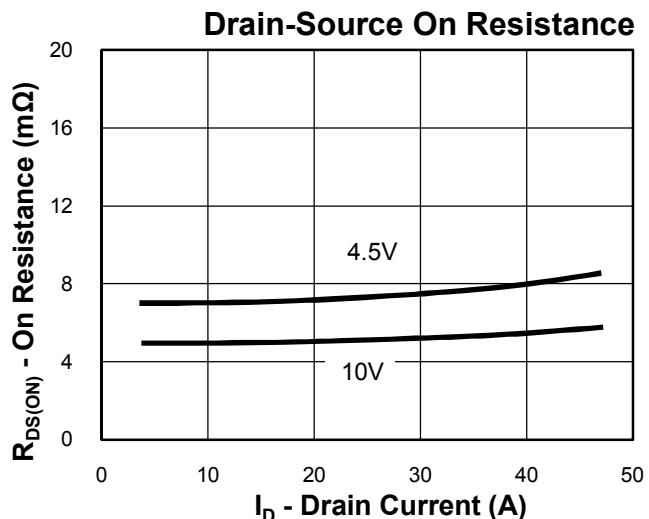
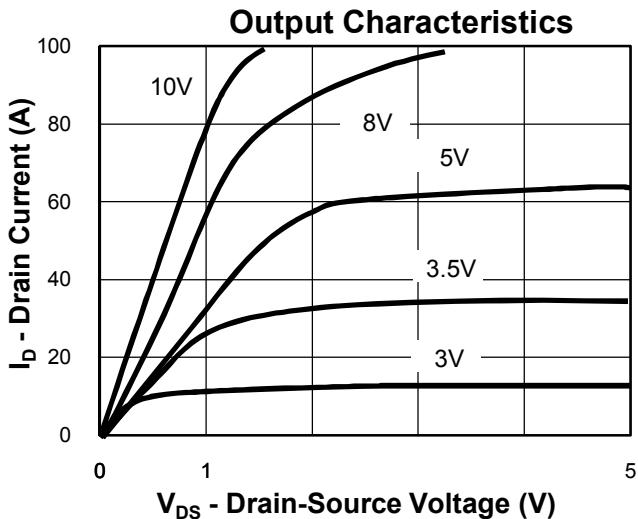
## Typical Characteristics



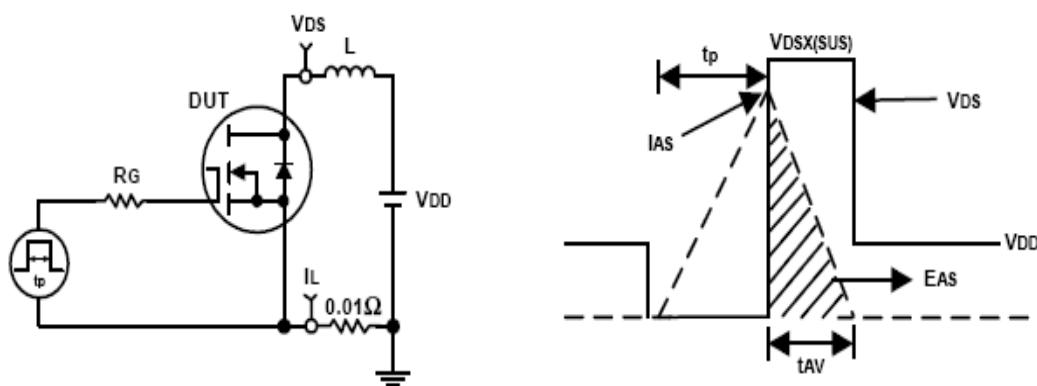
## Thermal Transient Impedance



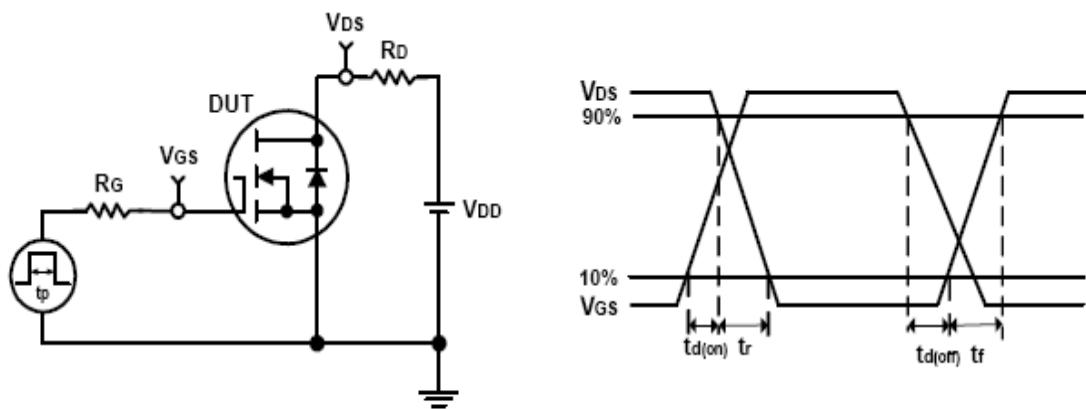
## Typical Characteristics



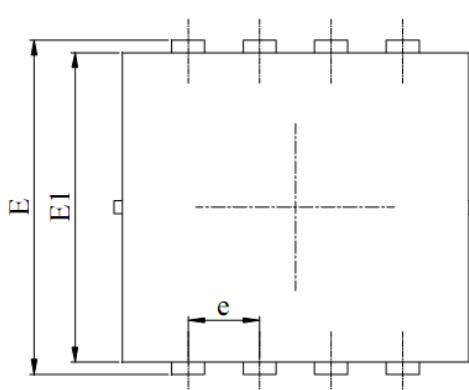
### Avalanche Test Circuit and Waveforms



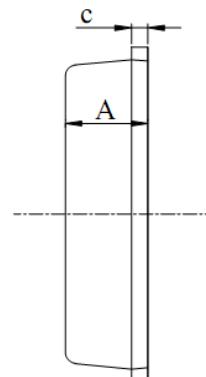
### Switching Time Test Circuit and Waveforms



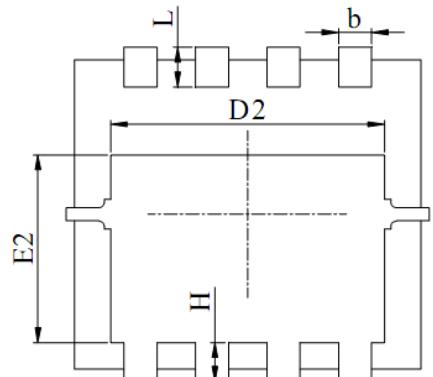
## Package Mechanical Data-PDFN3333-8L-Single



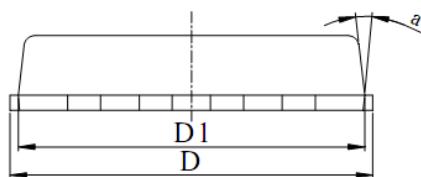
Top View



Side View



Bottom View

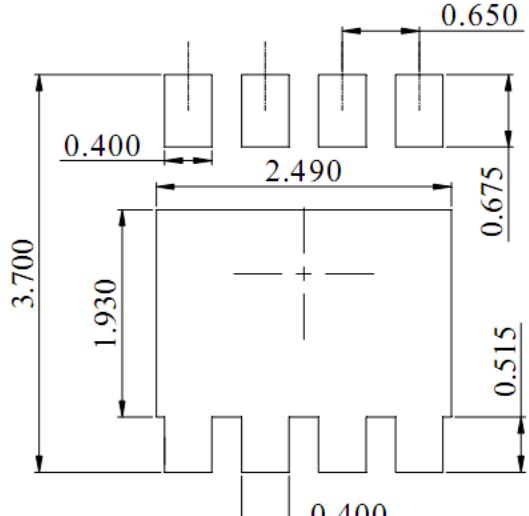


Front View

### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
2. ALL DIMNESIONS IN MILLIMETER (ANGLE IN DEGREE).
3. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS.

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.20	0.25
D	3.00	3.15	3.25
D1	2.95	3.05	3.15
D2	2.39	2.49	2.59
E	3.20	3.30	3.40
E1	2.95	3.05	3.15
E2	1.70	1.80	1.90
e	0.65 BSC		
H	0.30	0.40	0.50
L	0.25	0.40	0.50
a	---	---	15°



DIMENSIONS:MILLIMETERS