

**Features**

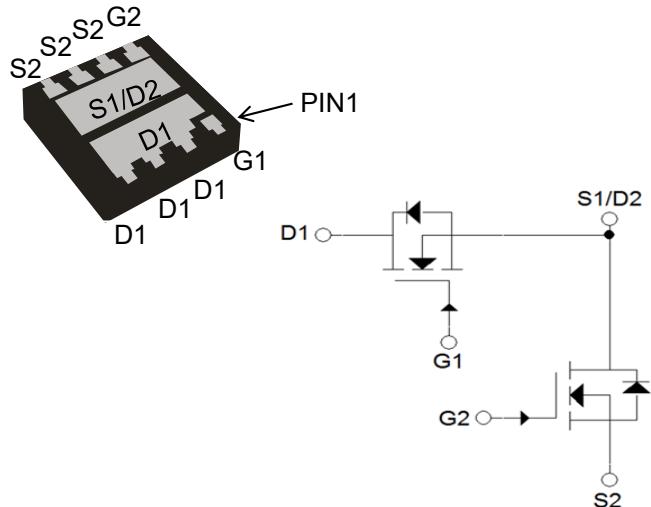
- ★ Split Gate Trench MOS Technology
- ★ 100% EAS Guaranteed
- ★ Fast Switching Speed
- ★ Green Device Available

**Product Summary**

BVDSS	RDS(ON)	ID
30V	6mΩ	50A

**Applications**

- ★ High Frequency Switching and Synchronous Rectification.
- ★ DC/DC Converter.

**DFN5060-8L Pin Configuration****Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>Gs</sub>	±20	V
Continuous Drain Current T <sub>C</sub> =25°C	I <sub>D</sub>	50	A
T <sub>C</sub> =100°C		41	
Pulsed Drain Current <sup>1</sup>	I <sub>DM</sub>	260	A
Single Pulse Avalanche Energy <sup>2</sup>	EAS	20	mJ
Total Power Dissipation	P <sub>D</sub>	31.25	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>3</sup>	R <sub>θJA</sub>	60	°C/W
Thermal Resistance from Junction-to-Case	R <sub>θJC</sub>	4	°C/W

## Dual N-Ch 30V MOSFETs

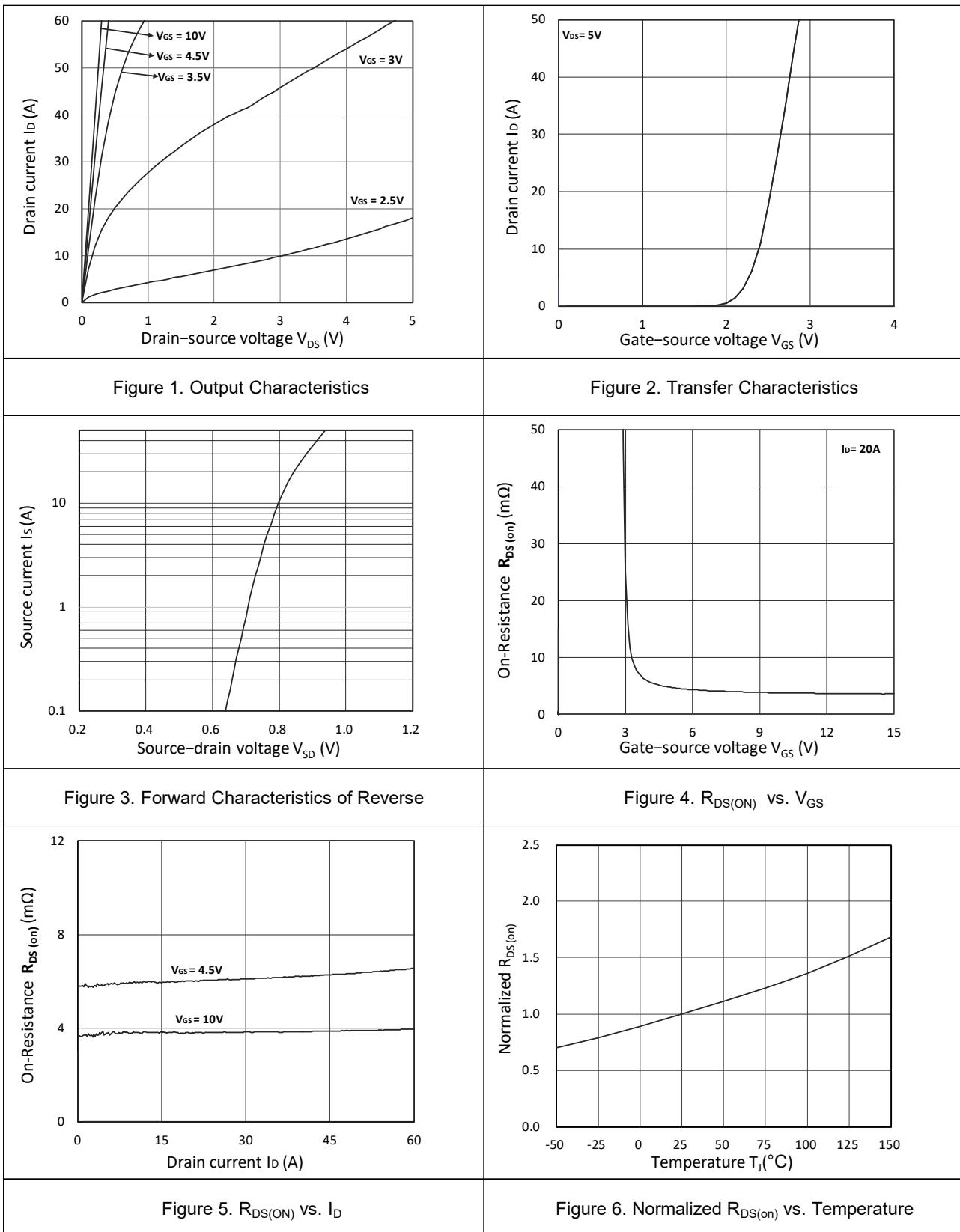
Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	30	-	-	V
Gate-body Leakage Current	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA
Zero Gate Voltage Drain Current $T_J=25^\circ\text{C}$ $T_J=100^\circ\text{C}$	$I_{DSS}$	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$	-	-	1	$\mu\text{A}$
			-	-	100	
Gate-Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.2	1.6	2.4	V
Drain-Source On-Resistance <sup>4</sup>	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$	-	6	8	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 10\text{A}$	-	7.5	9.8	
Forward Transconductance <sup>4</sup>	$g_{fs}$	$V_{DS} = 10\text{V}, I_D = 20\text{A}$	-	130	-	S
<b>Dynamic Characteristics<sup>5</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 15\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$	-	905	-	$\text{pF}$
Output Capacitance	$C_{oss}$		-	475	-	
Reverse Transfer Capacitance	$C_{rss}$		-	57	-	
Gate Resistance	$R_g$	$f = 1\text{MHz}$	-	1.9	-	$\Omega$
<b>Switching Characteristics<sup>5</sup></b>						
Total Gate Charge	$Q_g$	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}, I_D = 20\text{A}$	-	16	-	$\text{nC}$
Gate-Source Charge	$Q_{gs}$		-	3	-	
Gate-Drain Charge	$Q_{gd}$		-	3.3	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}, V_{DD} = 15\text{V}, R_G = 3\Omega, I_D = 20\text{A}$	-	6.3	-	$\text{ns}$
Rise Time	$t_r$		-	3.2	-	
Turn-Off Delay Time	$t_{d(off)}$		-	18	-	
Fall Time	$t_f$		-	3.6	-	
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 20\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	10	-	$\text{ns}$
Body Diode Reverse Recovery Charge	$Q_{rr}$		-	13.2	-	$\text{nC}$
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	$V_{SD}$	$I_S = 20\text{A}, V_{GS} = 0\text{V}$	-	-	1.2	V
Continuous Source Current	$T_C = 25^\circ\text{C}$	$I_S$	-	-	50	A

Notes:

- Repetitive rating, pulse width limited by junction temperature  $T_{J(\text{MAX})} = 150^\circ\text{C}$ .
- The EAS data shows Max. rating . The test condition is  $V_{DD}=25\text{V}, V_{GS}=10\text{V}, L=0.1\text{mH}, I_{AS}=20\text{A}$ .
- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
- This value is guaranteed by design hence it is not included in the production test.

## Typical Characteristics



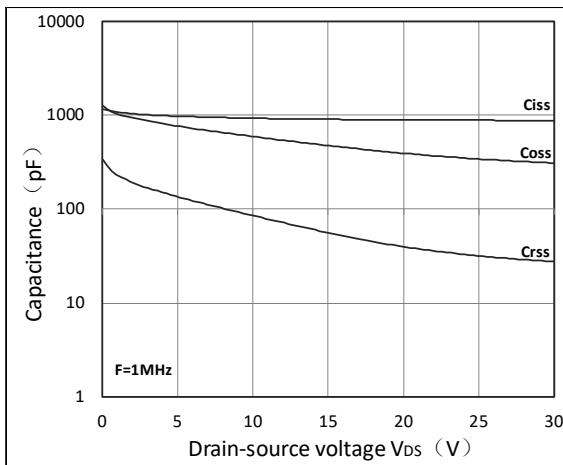


Figure 7. Capacitance Characteristics

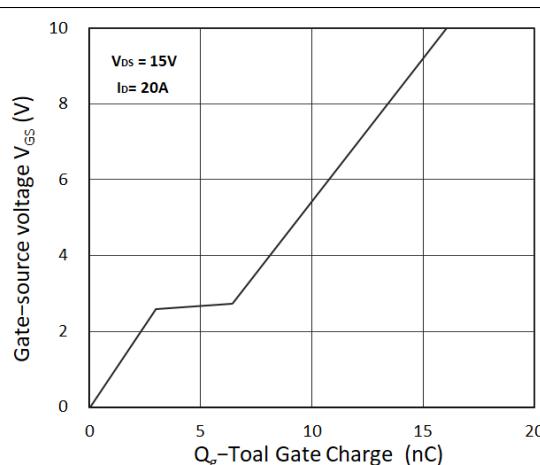


Figure 8. Gate Charge Characteristics

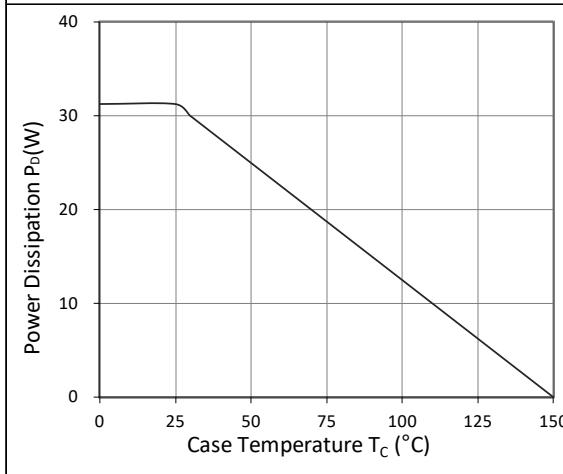


Figure 9. Power Dissipation

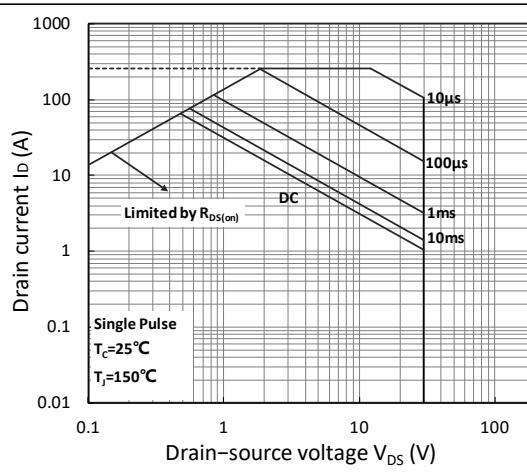


Figure 10. Safe Operating Area

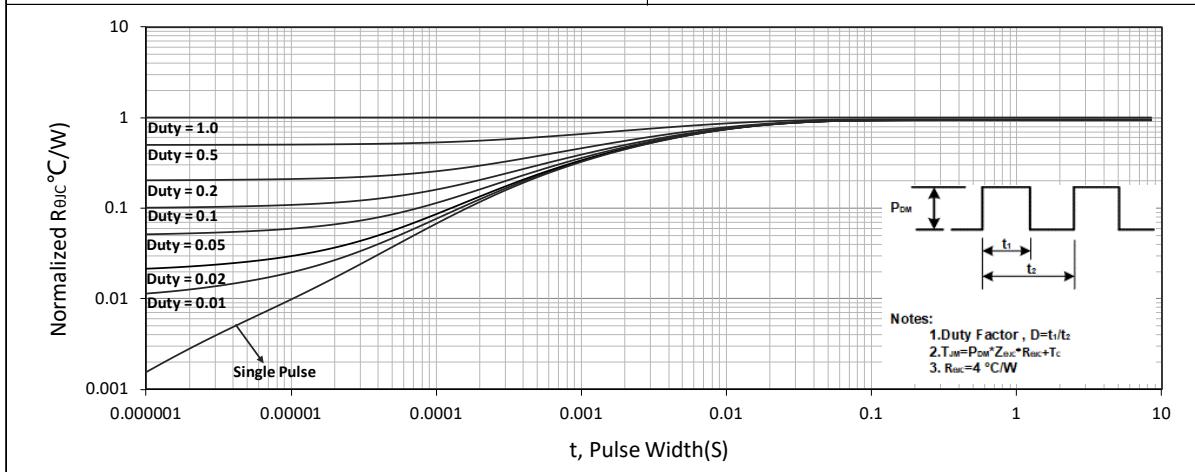
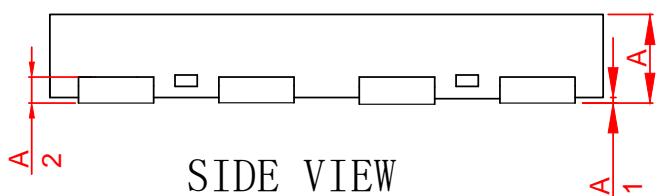
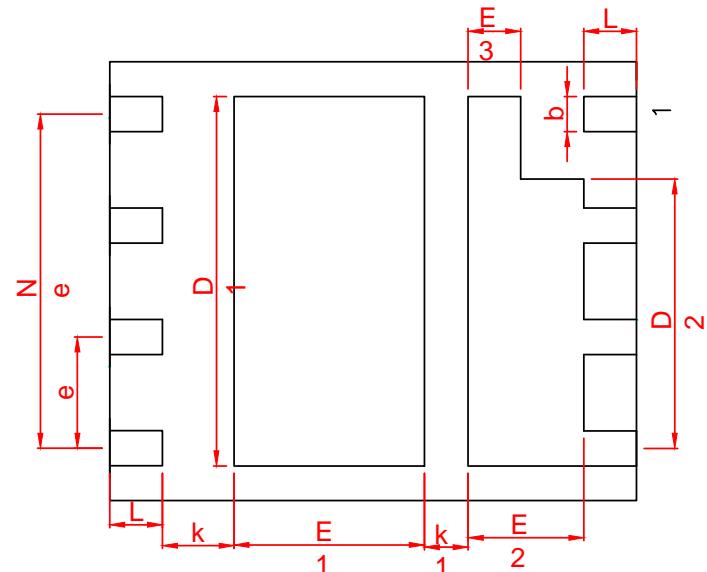
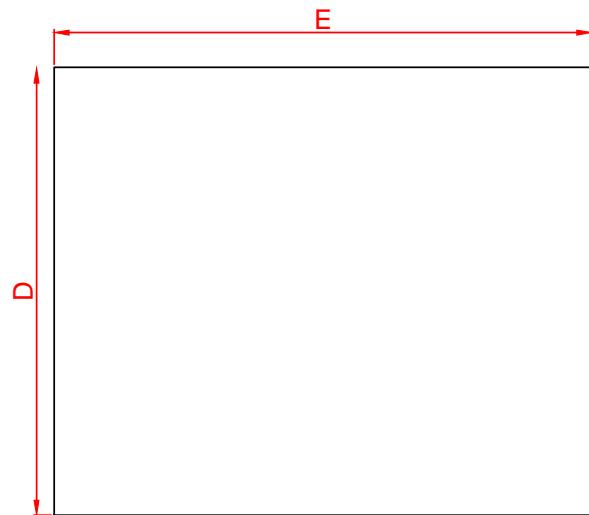


Figure 11. Normalized Maximum Transient Thermal Impedance

## DFN5060-8L Package Information



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.70	0.75	0.80
* A1	0.00	0.02	0.05
* b	0.36	0.41	0.46
* A2	0.203	BSC	
* D	4.90	5.00	5.10
* D1	4.15	4.20	4.25
* D2	2.87	3.07	3.27
* E	5.90	6.00	6.10
* E1	2.02	2.17	2.32
E2	1.22	1.32	1.42
E3	0.55	0.60	0.65
* e	1.27	REF	
* Ne	BSC 3.81		
k	0.71	0.81	0.91
* k1	0.40	0.50	0.60
* L	0.55	0.60	0.65