

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

Applications

- Power management in half bridge and inverters
- DC-DC Converter
- Load Switch

General Description

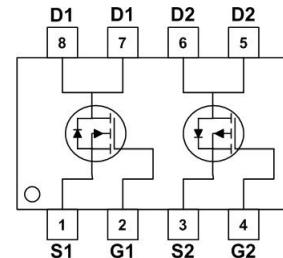
The XR4606C is the highest performance trench N-ch and P-ch MOSFETs with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The XR4606C meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

Product Summary

BVDSS	RDS(ON)	ID
20V	45mΩ	3A
-20V	80mΩ	- 3A

SOP-8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
V _{DS}	Drain-Source Voltage	20	-20	V
V _{GS}	Gate-Source Voltage	±12	±12	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	3.0	-3	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	2.0	-2	A
I _{DM}	Pulsed Drain Current ²	12	-10	A
P _D @T _C =25°C	Total Power Dissipation ⁴	2.5	2.08	W
T _{STG}	Storage Temperature Range	-55 to 150	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	105	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	50	°C/W

Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.4	0.7	1.0	V
R _{DS(on)} note2	Static Drain-Source on-Resistance	V _{GS} =4.5V, I _D =3A	-	45	55	mΩ
		V _{GS} =2.5V, I _D =2A	-	62	85	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=1.0MHz	-	184	-	pF
C _{oss}	Output Capacitance		-	38	-	pF
C _{rss}	Reverse Transfer Capacitance		-	28	-	pF
Q _g	Total Gate Charge	V _{DS} =10V, I _D =3A, V _{GS} =4.5V	-	2.7	-	nC
Q _{gs}	Gate-Source Charge		-	0.4	-	nC
Q _{gd}	Gate-Drain("Miller") Charge		-	0.5	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =10V, I _D =3A, R _{GEN} =3Ω, V _{GS} =4.5V	-	8	-	ns
t _r	Turn-on Rise Time		-	27	-	ns
t _{d(off)}	Turn-off Delay Time		-	26	-	ns
t _f	Turn-off Fall Time		-	33	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	3	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	12	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =3A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

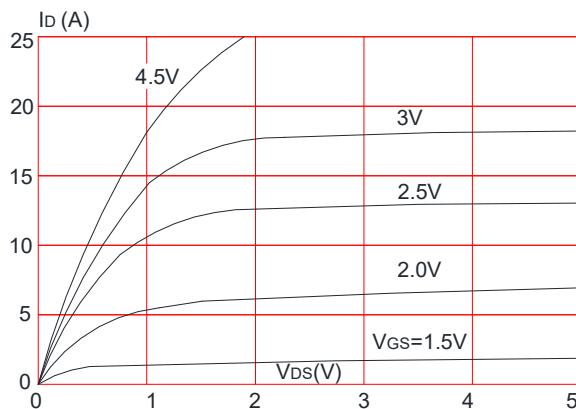
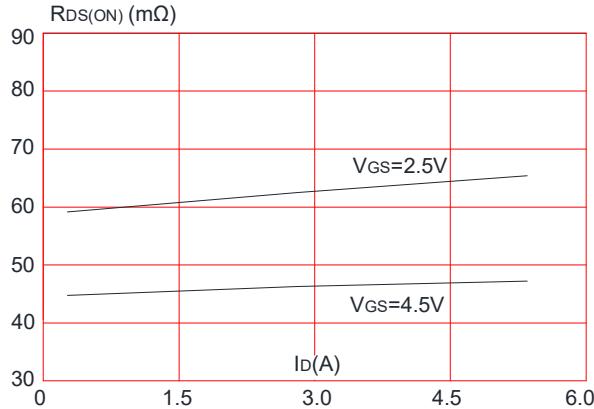
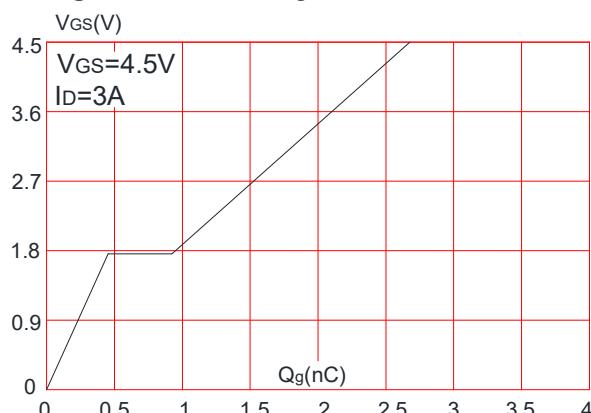
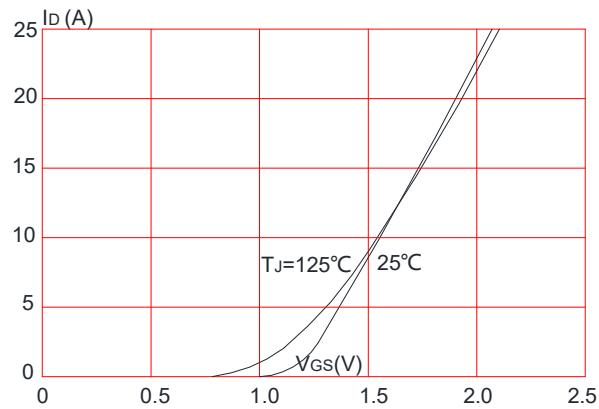
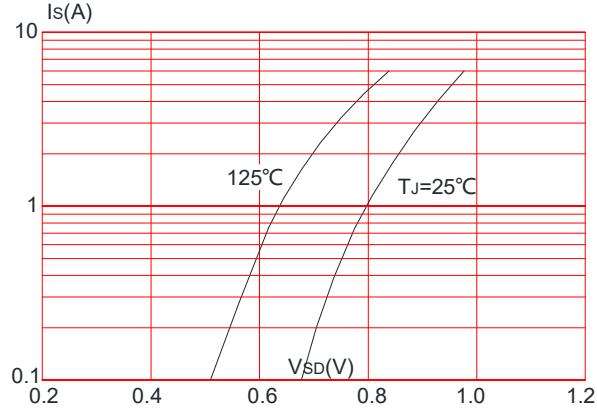
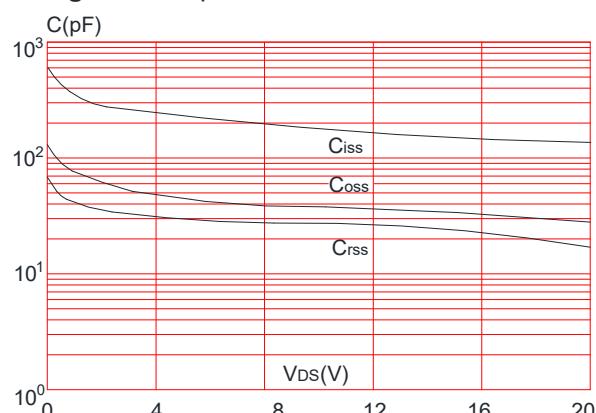
2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D = -250\mu\text{A}$	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -20\text{V}, V_{GS} = 0\text{V},$	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 12\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-0.4	-0.7	-1.0	V
$R_{DS(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{GS} = -4.5\text{V}, I_D = -2.5\text{A}$	-	80	104	$\text{m}\Omega$
		$V_{GS} = -2.5\text{V}, I_D = -1.5\text{A}$	-	110	154	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = -10\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	-	248	-	pF
C_{oss}	Output Capacitance		-	42	-	pF
C_{rss}	Reverse Transfer Capacitance		-	31	-	pF
Q_g	Total Gate Charge		-	2.9	-	nC
Q_{gs}	Gate-Source Charge	$V_{DS} = -10\text{V}, I_D = -2.5\text{A}, V_{GS} = -4.5\text{V}$	-	0.45	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	0.75	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = -10\text{V}, R_L = 5\Omega, R_{GEN} = 3\Omega, V_{GS} = -4.5\text{V}$	-	9.8	-	ns
t_r	Turn-on Rise Time		-	4.9	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	20.5	-	ns
t_f	Turn-off Fall Time		-	7	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	-3.0	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-10	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_s = -2.5\text{A}$	-	-	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

N-Channel Typical Characteristics**Figure 1:** Output Characteristics**Figure 3:** On-resistance vs. Drain Current**Figure 5: Gate Charge Characteristics****Figure 2:** Typical Transfer Characteristics**Figure 4: Body Diode Characteristics****Figure 6: Capacitance Characteristics**

N-Channel Typical Characteristics

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

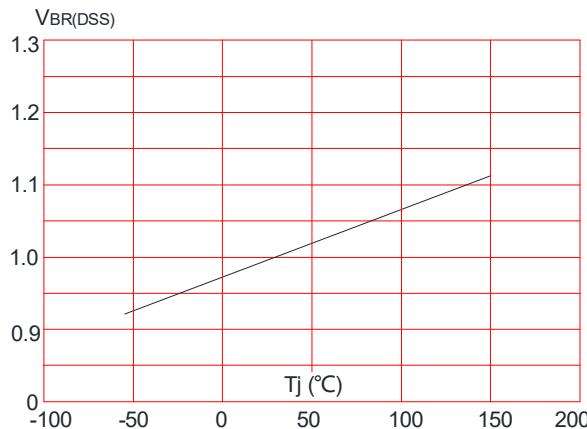


Figure 9: Maximum Safe Operating Area

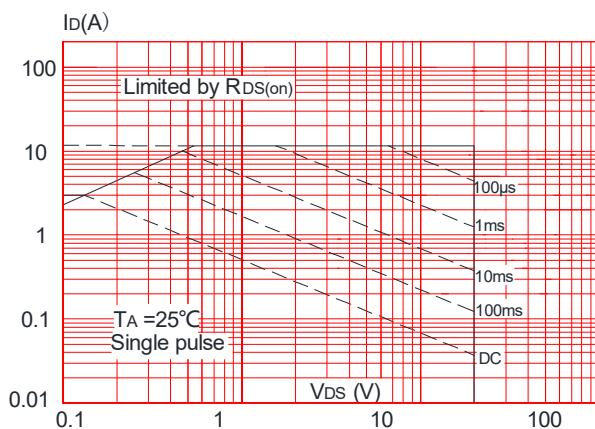


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

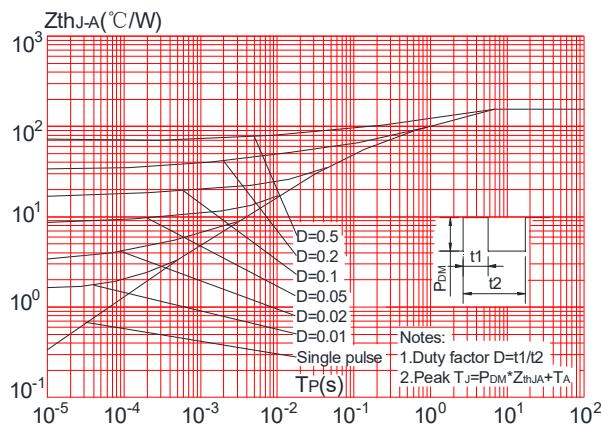


Figure 8: Normalized on Resistance vs. Junction Temperature

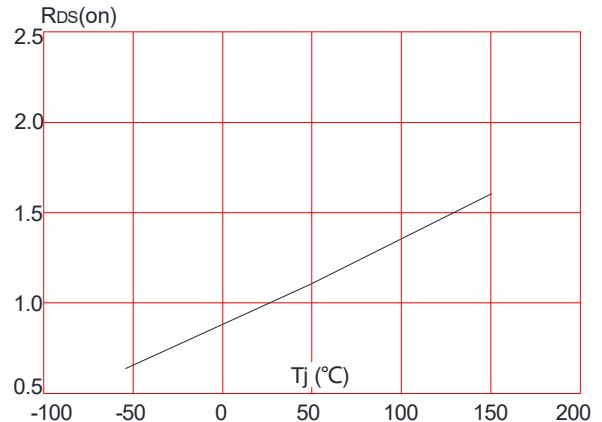
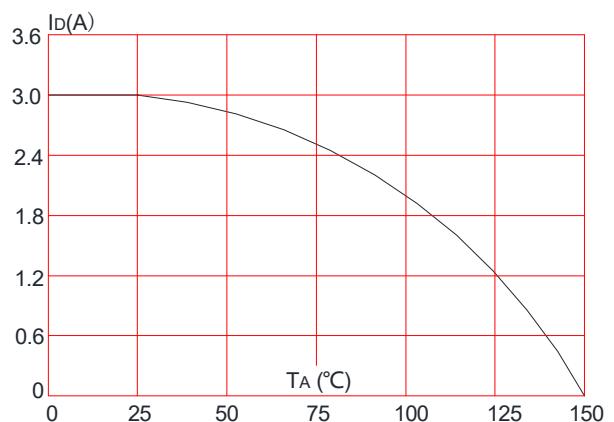


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature



P-Channel Typical Characteristics

Figure 1: Output Characteristics

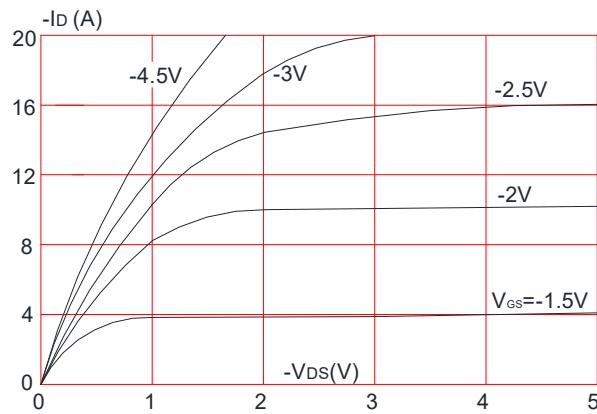


Figure 3: On-resistance vs. Drain Current
 $R_{DS(ON)}$ (mΩ)

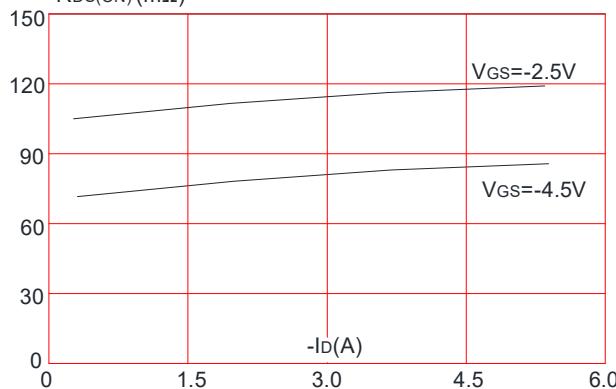


Figure 5: Gate Charge Characteristics

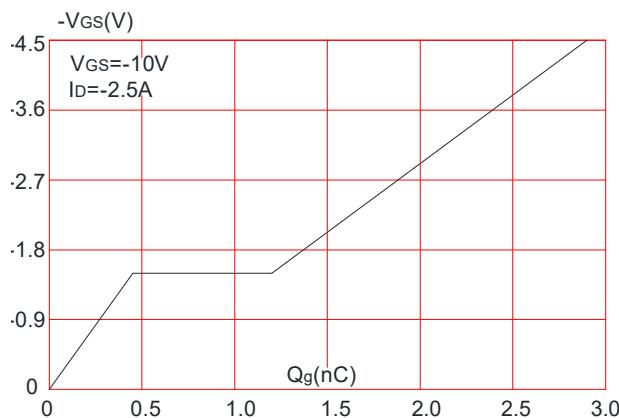


Figure 2: Typical Transfer Characteristics

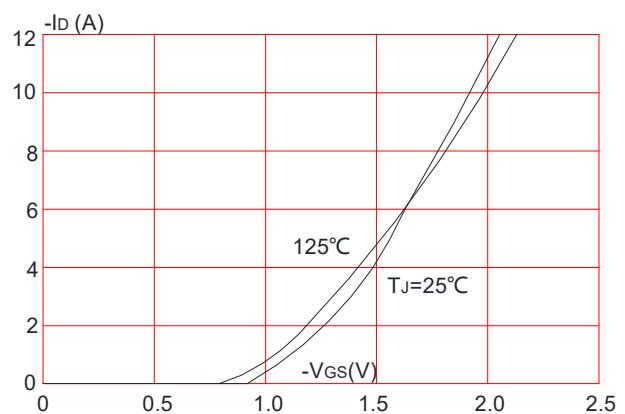


Figure 4: Body Diode Characteristics

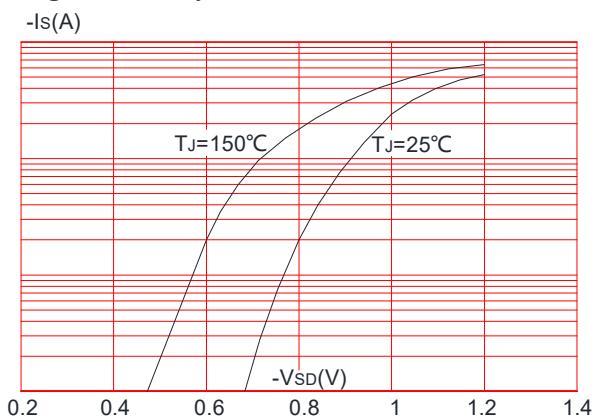
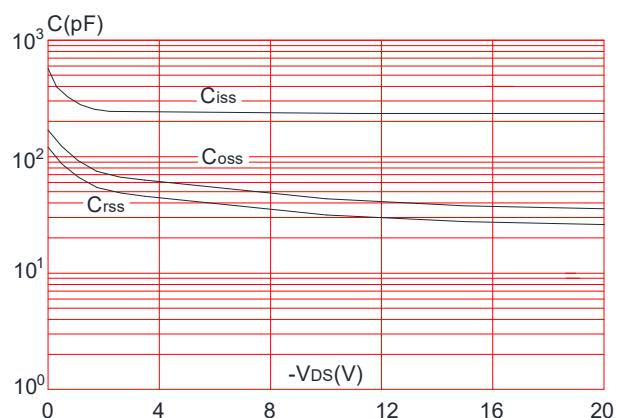


Figure 6: Capacitance Characteristics



P-Channel Typical Characteristics

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

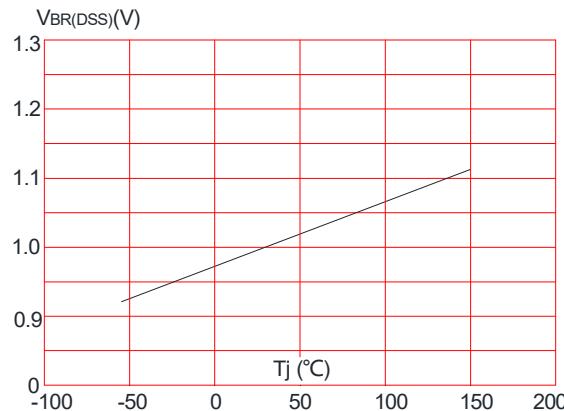


Figure 9: Maximum Safe Operating Area

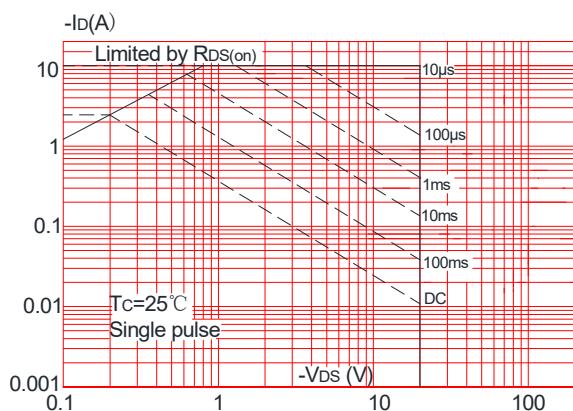


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

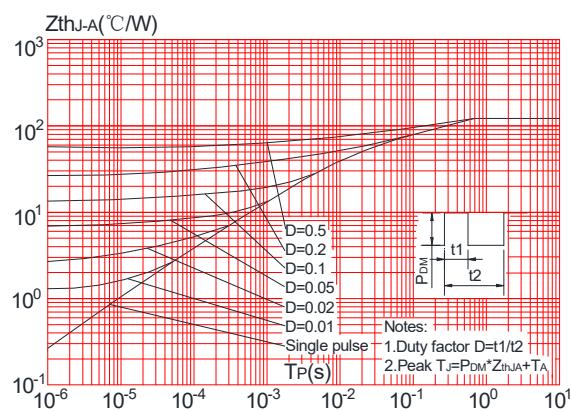


Figure 8: Normalized on Resistance vs. Junction Temperature

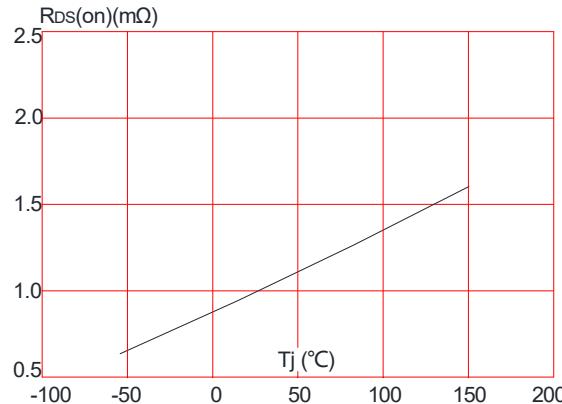
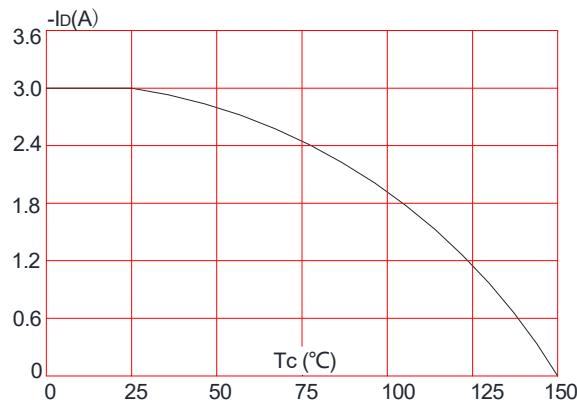
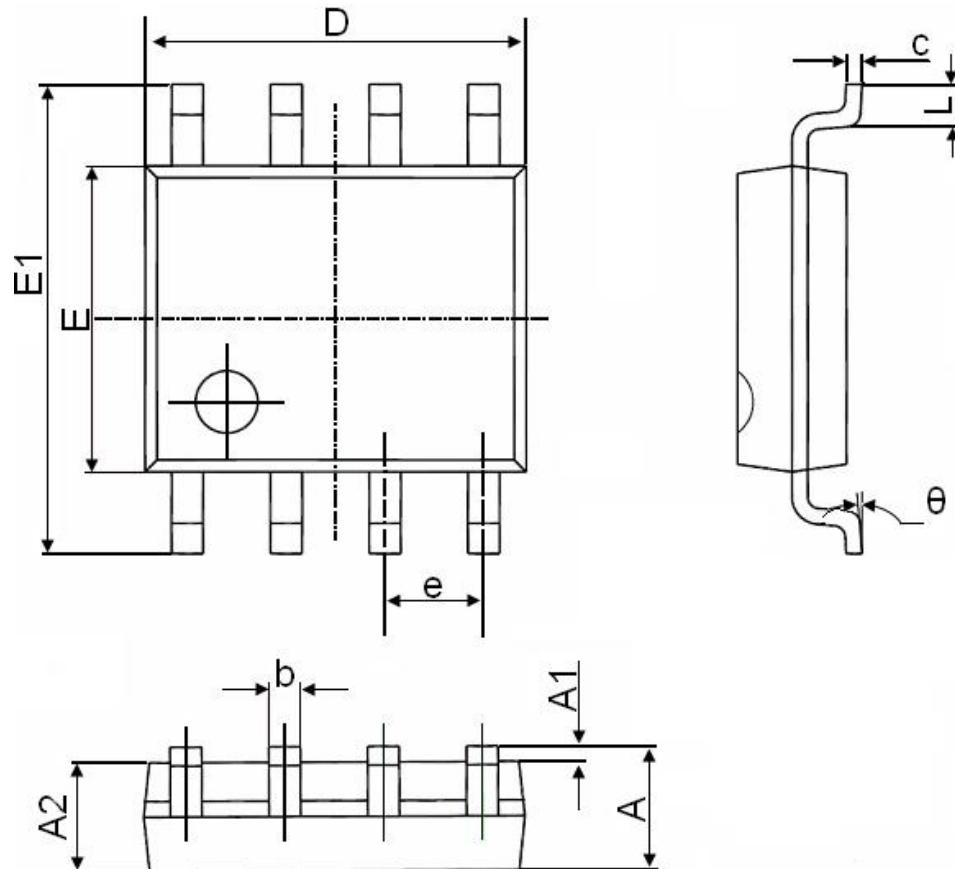


Figure 10: Maximum Continuous Drain Current vs. Case Temperature



SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°