N-Ch and P-Ch Fast Switching MOSFETs

★ Green Device Available
★ Super Low Gate Charge
★ Excellent CdV/dt effect decline
★ Advanced high cell density Trench technology

Product Summary

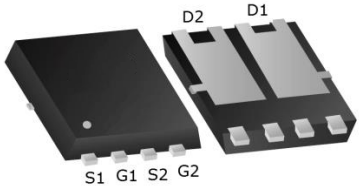
Green

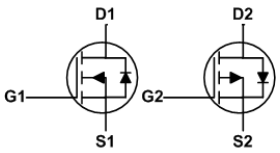
BVDSS	RDSON	ID
20V	12mΩ	20A
-20V	16mΩ	-20A

Description

The XR20G02D is the high performance complementary N-ch and P-ch MOSFETs with high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.
The XR20G02D meet the RoHS and Green

PDFN3333-8L Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage	20	-20	V
V_{GS}	Gate-Source Voltage	±12	±12	V
I_D@Ta=25℃	Continuous Drain Current, V_{GS} @ 10V[1]	20	-20	A
I_D@Ta=100℃	Continuous Drain Current, V_{GS} @ 10V[1]	13	-13	A
I_{DM}	Pulsed Drain Current[2]	85	-50	A
EAS	Single Pulse Avalanche Energy[3]	---	---	mJ
P_D@T_C=25℃	Total Power Dissipation[4]	2	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	115	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	---	℃/W

N-Channel Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	20	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BVDSS Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	---	---	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=4.5V , I_D=21A	---	12	16	mΩ
		V_{GS}=2.5V , I_D=13A	---	16	20.5	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	0.5	0.75	1.2	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	---	---	mV/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=20V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}20V , V_{GS}=0V , T_J=55℃	---	---	---	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±12V , V_{DS}=0V	---	---	±100	nA
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	---	---	Ω
Q_g	Total Gate Charge (4.5V)	V_{DS}=15V , V_{GS}=4.5V , I_D=10A	---	15	---	nC
Q_{gs}	Gate-Source Charge		---	2	---	
Q_{gd}	Gate-Drain Charge		---	5.2	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=10V , V_{GS}=4.5V , R_G=3Ω , I $_D$=10A	---	9	---	ns
T_r	Rise Time		---	25	---	
$T_{d(off)}$	Turn-Off Delay Time		---	37	---	
T_f	Fall Time		---	14	---	
C_{iss}	Input Capacitance	V_{DS}=10V , V_{GS}=0V , f=1MHz	---	700	---	pF
C_{oss}	Output Capacitance		---	132	---	
C_{rss}	Reverse Transfer Capacitance		---	114	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,4]	V_G=V_D=0V , Force Current	---	---	20	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=21A , T_J=25C	---	---	1.2	V
t_{rr}	Reverse Recovery Time	IF=21A ,di/dt=100A / μs , T_J= 2 5 C	---	---	---	nS
Q_{rr}	Reverse Recovery Charge		---	---	---	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

P-Channel Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=-1mA	---	---	---	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-4.5V , I_D=-13A	---	16	21	mΩ
		V_{GS}=-2.5V , I_D=-10A	---	22	29	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-0.4	-0.7	-1	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	---	---	mV/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-20V , V_{GS}=0V , T_J=25℃	---	---	-1	uA
		V_{DS}=-20V, V_{GS}=0V , T_J=100℃	---	---	---	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±12V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=-5V , I_D=-20A	---	---	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	---	---	Ω
Q_g	Total Gate Charge	V_{DS}=-10V , V_{GS}=-4.5V , I_D=-13A	---	33.7	---	nC
Q_{gs}	Gate-Source Charge		---	3.5	---	
Q_{gd}	Gate-Drain Charge		---	10.5	---	
$T_{d(on)}$	Turn-On Delay Time	V_{GS}=-4.5V, V_{DS}=-10V, I_D=-13A, R_{GEN}=10Ω	---	11	---	ns
T_r	Rise Time		---	35	---	
$T_{d(off)}$	Turn-Off Delay Time		---	30	---	
T_f	Fall Time		---	10	---	
C_{iss}	Input Capacitance	V_{DS}=-10V , V_{GS}=0V , f=1MHz	---	1200	---	pF
C_{oss}	Output Capacitance		---	191	---	
C_{rss}	Reverse Transfer Capacitance		---	168	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,5]	V_G=V_D=0V , Force Current	---	---	-20	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=-13A , T_J=25℃	---	---	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Performance Characteristics-N

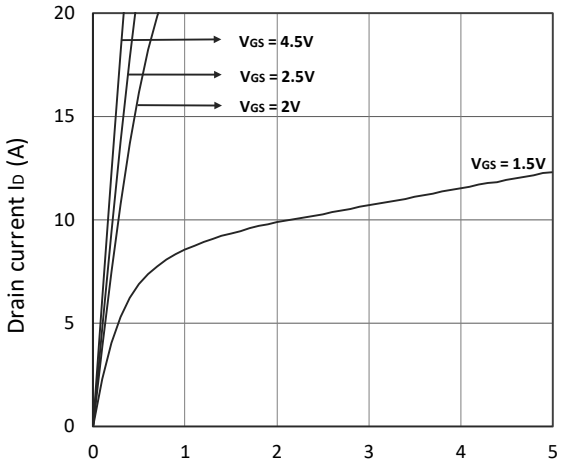


Figure 1. Output Characteristics

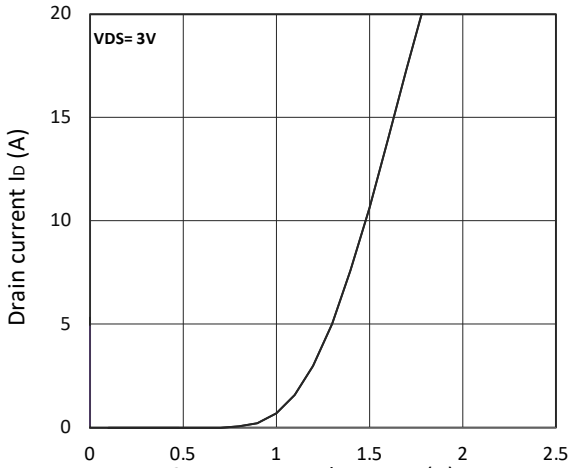


Figure 2. Transfer Characteristics

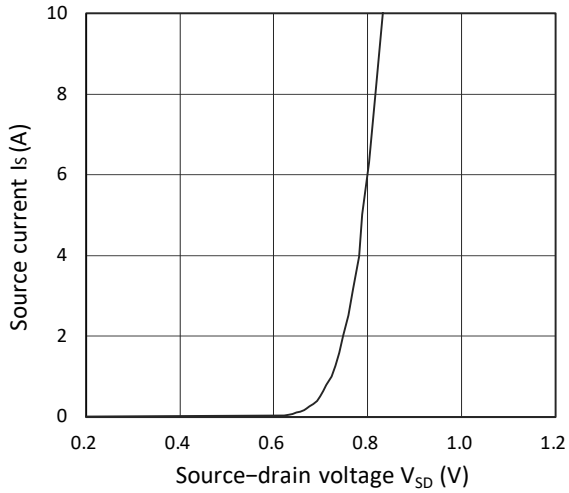


Figure 3. Forward Characteristics of Reverse

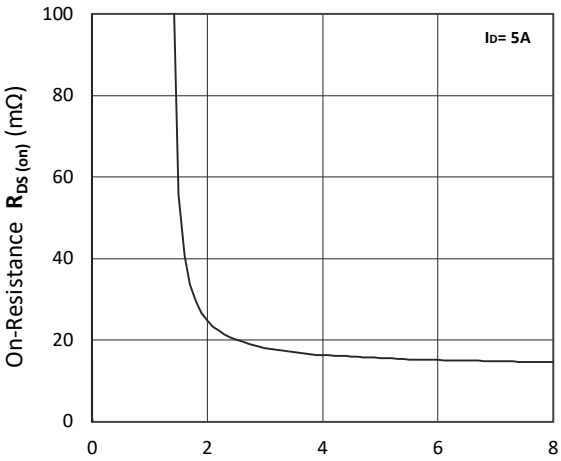


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

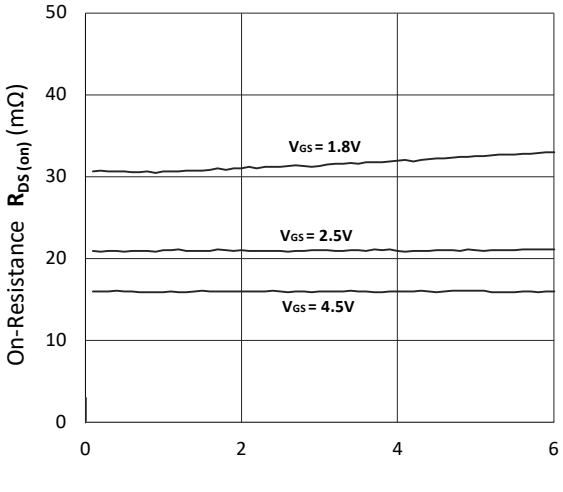


Figure 5. $R_{DS(ON)}$ vs. I_D

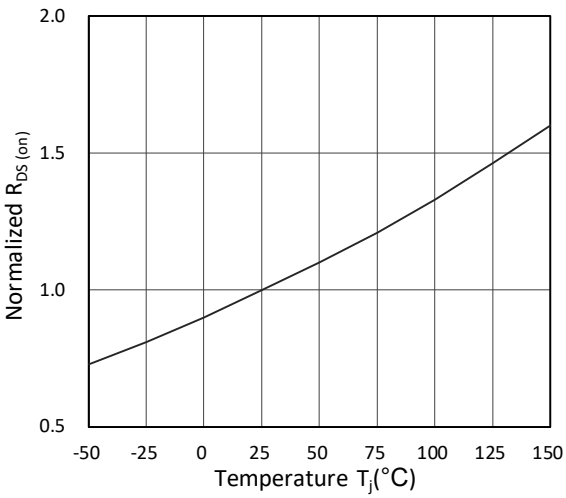


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

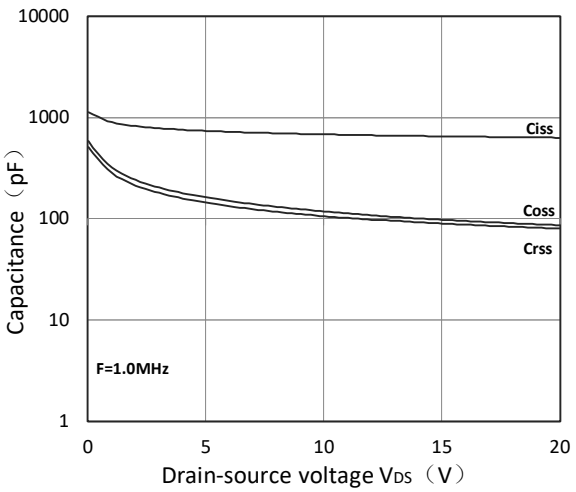


Figure 7. Capacitance Characteristics

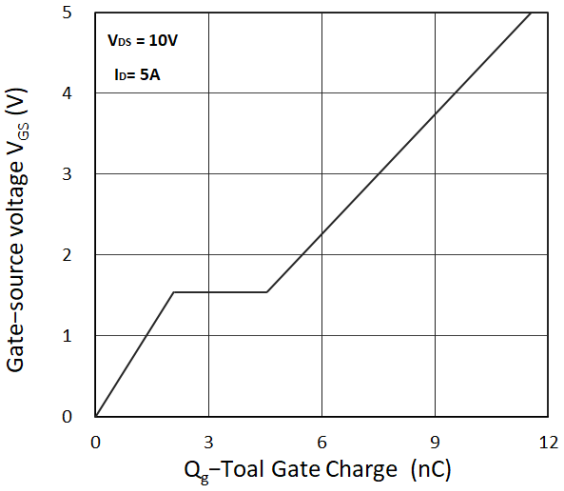


Figure 8. Gate Charge Characteristics

Typical Performance Characteristics-P

Figure1: Output Characteristics

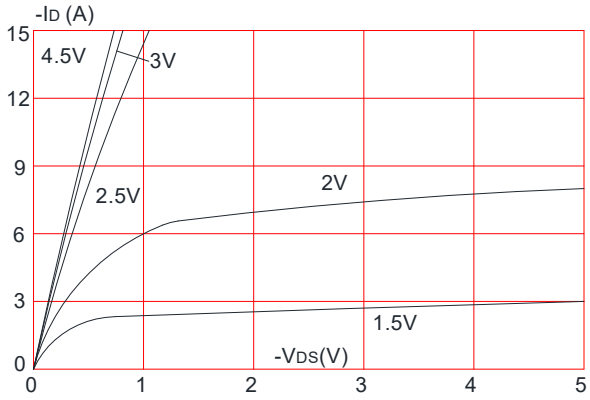


Figure 2: Typical Transfer Characteristics

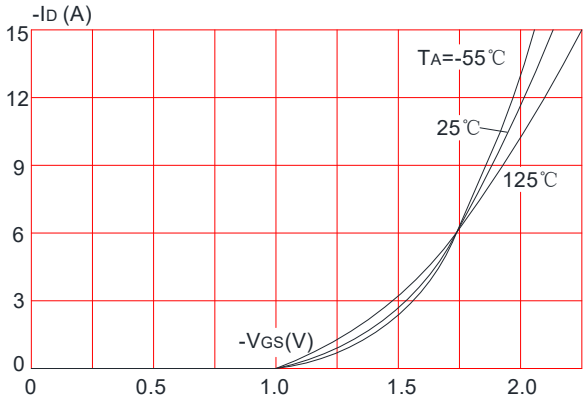


Figure 3: On-resistance vs. Drain Current

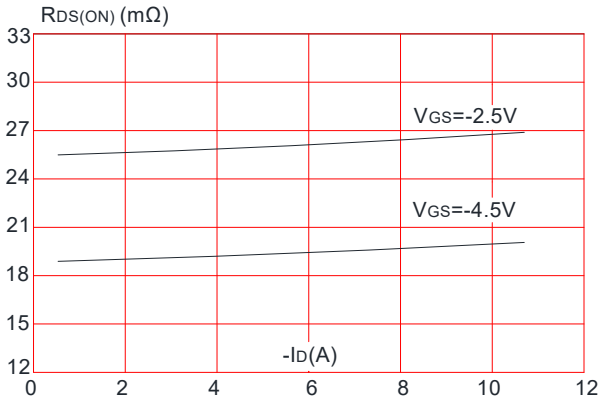


Figure 4: Body Diode Characteristics

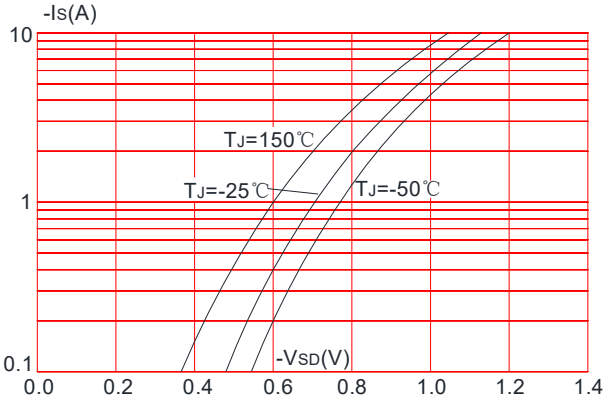


Figure 5: Gate Charge Characteristics

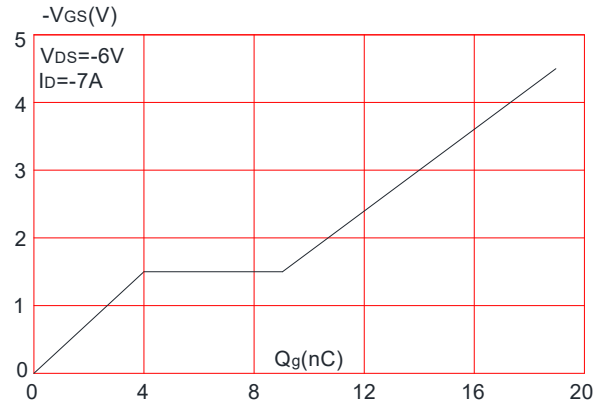


Figure 6: Capacitance Characteristics

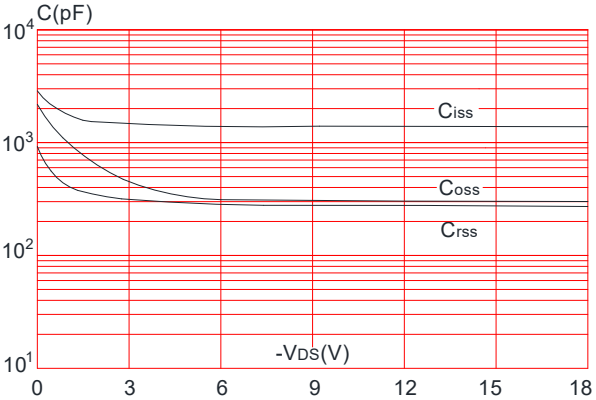


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

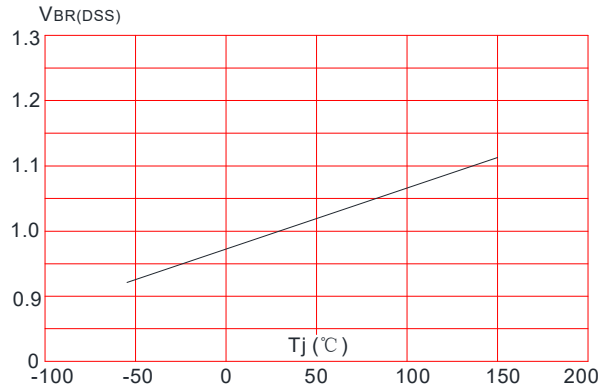


Figure 8: Normalized on Resistance vs. Junction Temperature

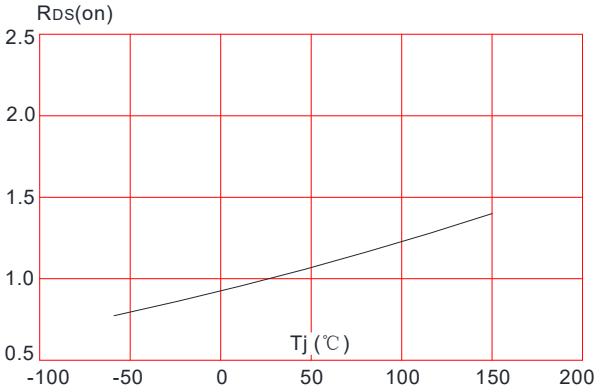


Figure 9: Maximum Safe Operating Area

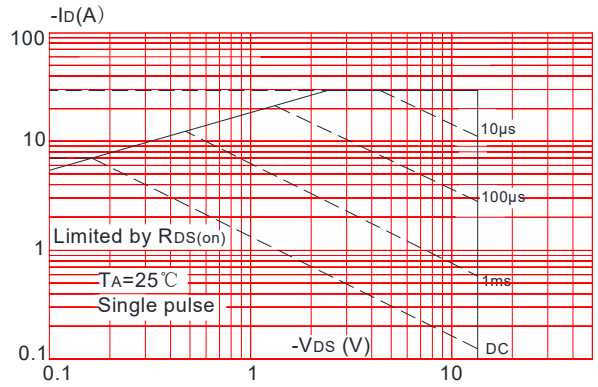


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

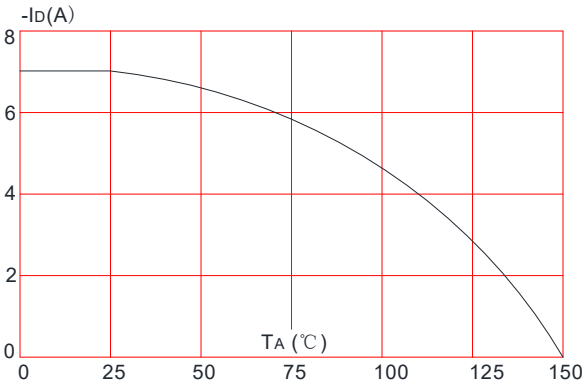
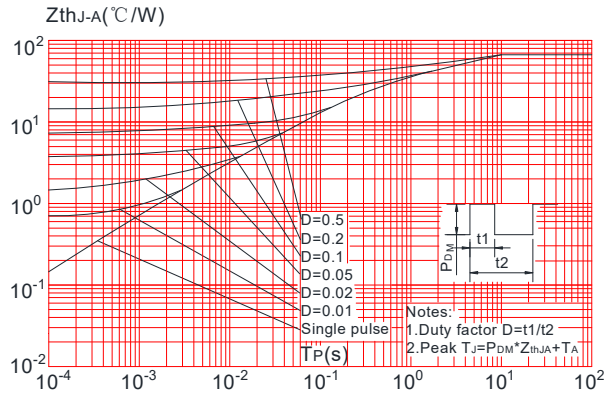
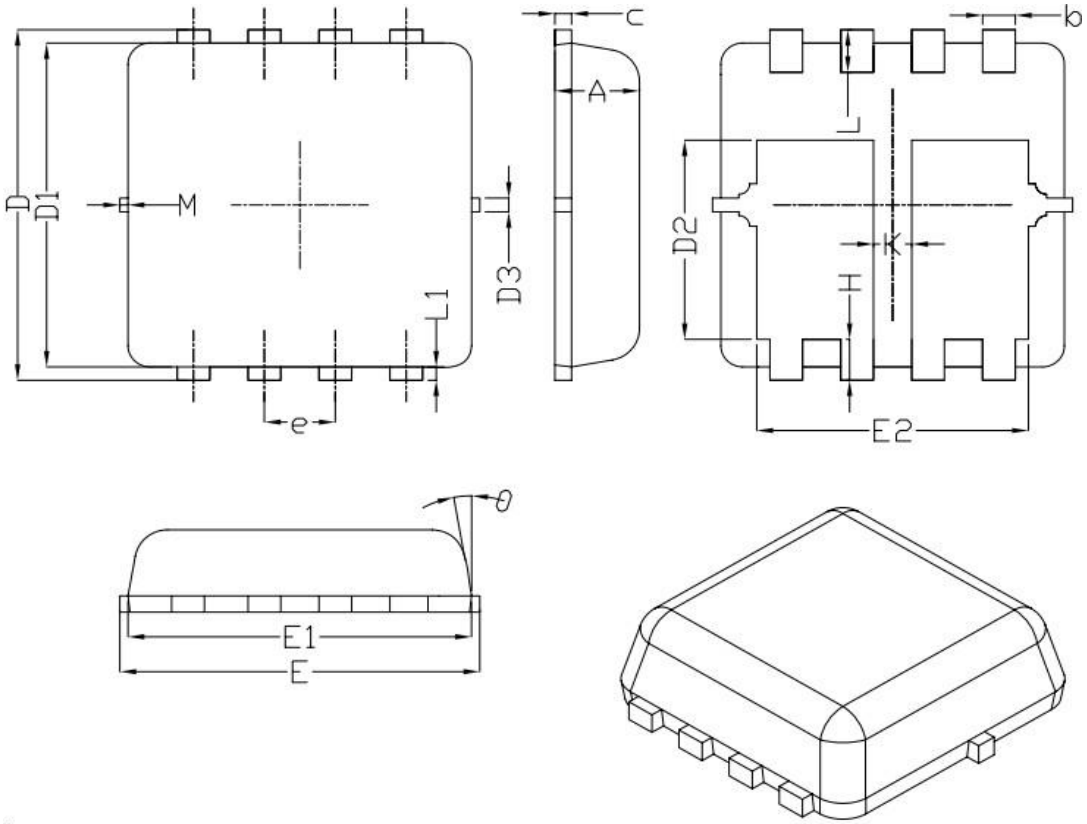


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Dual PDFN3333-8L Package Outline Data



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	--	0.13	--
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65 BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	--	0.13	--
K	0.30	--	--
θ	--	10°	12°
M	*	*	0.15
* Not Specified			

Notes:
1. Refer to JEDEC MO-240 variation CA.
2. Dimensions "D1" and "E1" do NOT include mold flash protrusions or gate burrs.
3. Dimensions "D1" and "E1" include interterminal flash or protrusion.